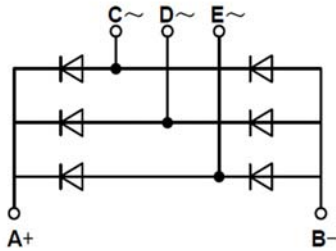


PRODUCT FEATURES

- ☐ Low Forward Voltage
- ☐ High Surge Current Capability
- ☐ Low Leakage Current
- ☐ Low Inductance Package



APPLICATIONS

- ☐ Field Supply For DC Motors
- ☐ Line Rectifiers For Transistorized AC Motor Controllers
- ☐ Non-controllable Rectifiers For AC/DC Converter
- ☐ UL:E332185



Module Type

Module Type	V_{RRM} Repetitive Peak Reverse Voltage	V_{RSM} Non-Repetitive Peak Reverse Voltage	Unit
MMD150F180X	1800	1900	V

ABSOLUTE MAXIMUM RATINGS($T_c=25^{\circ}C$ unless otherwise specified)

Symbol	Parameter/Test Conditions		Values	Unit
I_D	Output Current(D.C.)	Three phase, full wave, $T_c=95^{\circ}C$	150	A
I_{FSM}	Non-Repetitive Surge Forward Current	1/2 cycle, 50HZ, peak value, $T_J=45^{\circ}C$	1500	
		1/2 cycle, 60HZ, peak value, $T_J=45^{\circ}C$	1600	
I^2t	For Fusing	1/2 cycle, 50HZ, peak value, $T_J=45^{\circ}C$	11.2	KA^2S
		1/2 cycle, 60HZ, peak value, $T_J=45^{\circ}C$	10.6	
P_D	Power Dissipation		1136	W
T_J	Junction Temperature		-40 to +150	$^{\circ}C$
T_{STG}	Storage Temperature Range		-40 to +125	$^{\circ}C$
V_{ISO}	Isolation Breakdown Voltage	AC, 50Hz(R.M.S), t=1minute	3000	V
Torque	Module to Sink	Recommended (M6)	3~5	Nm
Torque	Module Electrodes	Recommended (M6)	3~5	Nm
R_{thJC}	Junction to Case Thermal Resistance	per diode	0.63	K /W
		per module	0.11	
Weight			250	g

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MMD150F180X

ELECTRICAL CHARACTERISTICS ($T_C = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter/Test Conditions		Min.	Typ.	Max.	Unit
I_{RM}	Maximum Reverse Leakage Current	$V_R = V_{RRM}$			0.5	mA
		$V_R = V_{RRM}, T_J = 125^\circ\text{C}$			10	
V_F	Forward Voltage Drop	$I_F = 150\text{A}$			1.45	V
V_{TO}	For power loss calculations only, $T_J = 125^\circ\text{C}$				0.9	V
r_T					3.5	m Ω

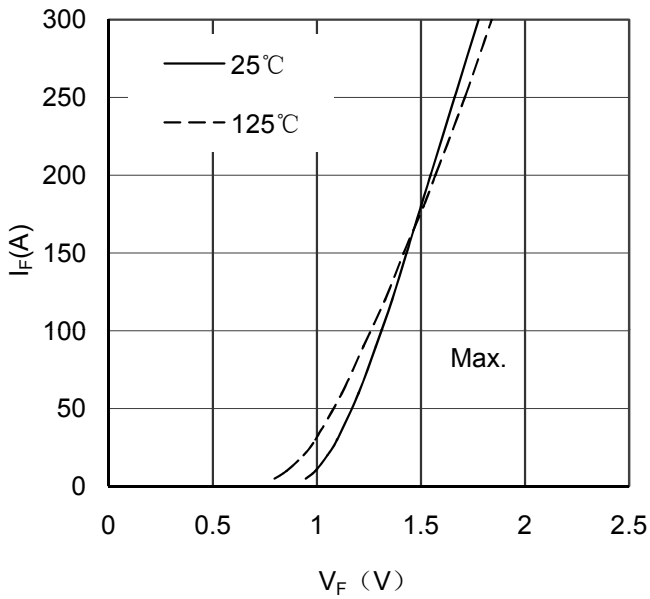


Figure 1. Forward Voltage Drop vs Forward Current

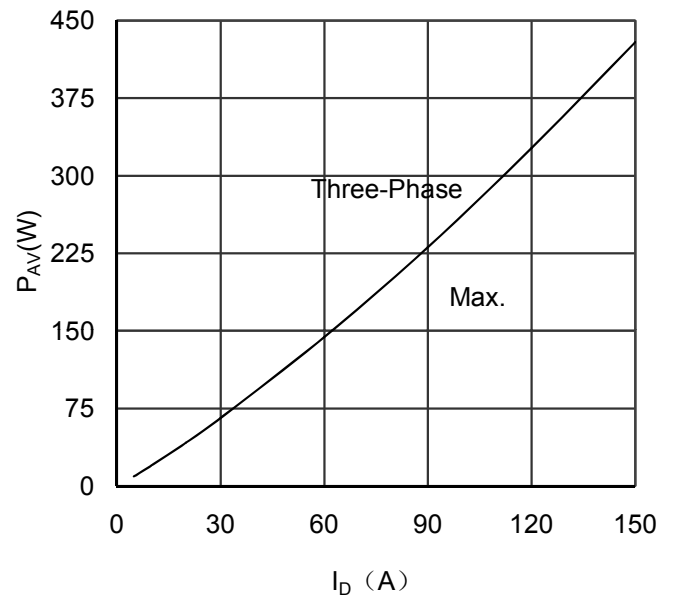


Figure 2. Power dissipation vs Output Current

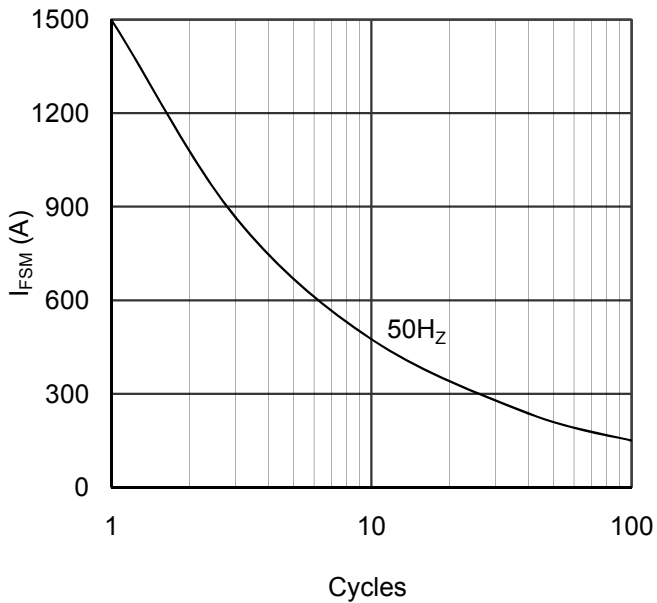


Figure 3. Max Non-Repetitive Forward Surge Current

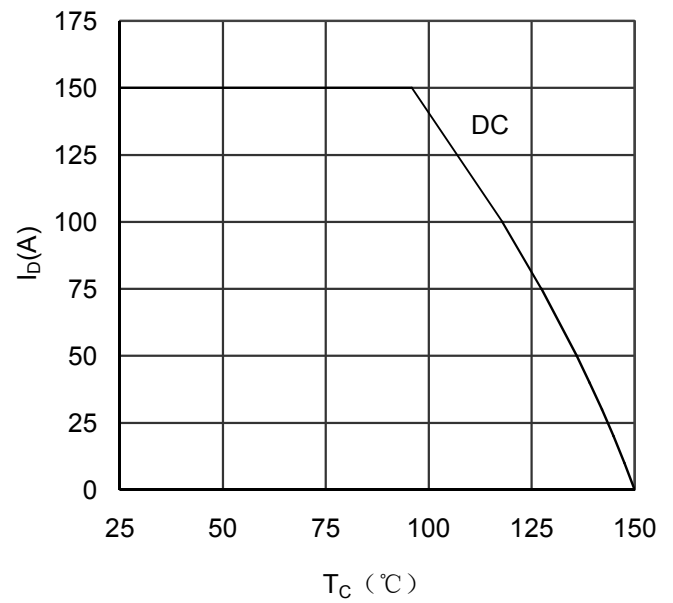


Figure 4. Output current vs Case temperature

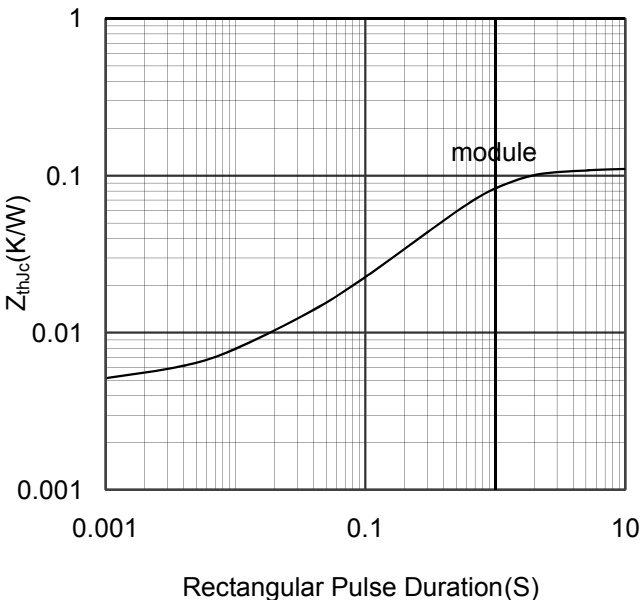


Figure 5. Transient Thermal Impedance

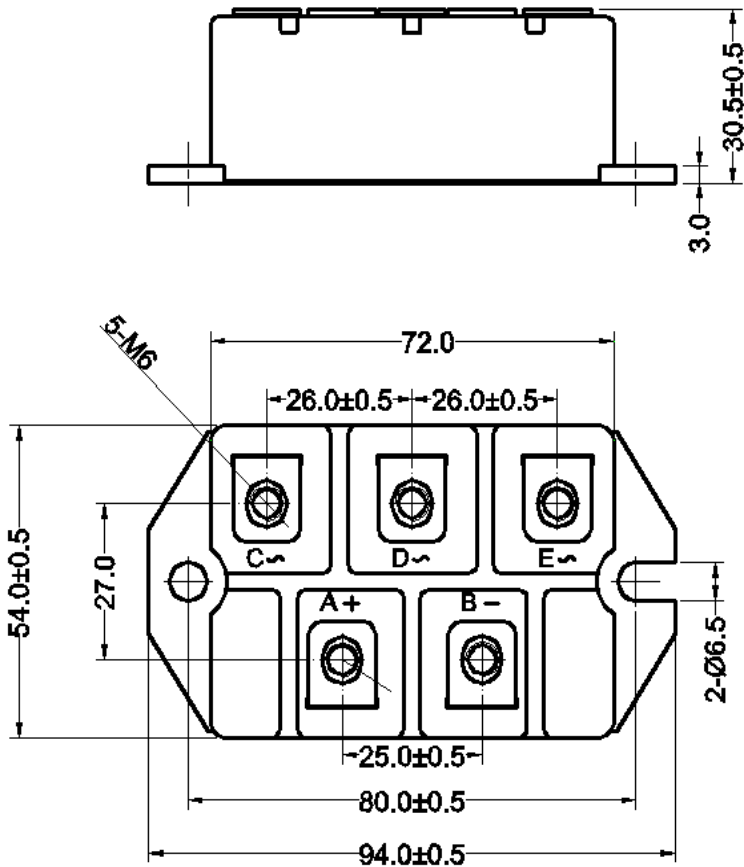


Figure 6. Package Outline