

# 2N3742

CASE 79-02, STYLE 1  
TO-39 (TO-205AD)

AMPLIFIER TRANSISTOR

NPN SILICON

## MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	V <sub>CEO</sub>	300	Vdc
Collector-Base Voltage	V <sub>CBO</sub>	300	Vdc
Emitter-Base Voltage	V <sub>EBO</sub>	7.0	Vdc
Collector Current — Continuous	I <sub>C</sub>	50	mAdc
Total Device Dissipation @ T <sub>A</sub> = 25°C Derate above 25°C	P <sub>D</sub>	1.0 5.71	Watt mW/°C
Total Device Dissipation @ T <sub>C</sub> = 25°C Derate above 25°C	P <sub>D</sub>	5.0 28.6	Watts mW/°C
Operating and Storage Junction Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-65 to +200	°C

## THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	R <sub>θJC</sub>	35	°C/W
Thermal Resistance, Junction to Ambient	R <sub>θJA</sub>	175	°C/W

## ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C unless otherwise noted.)

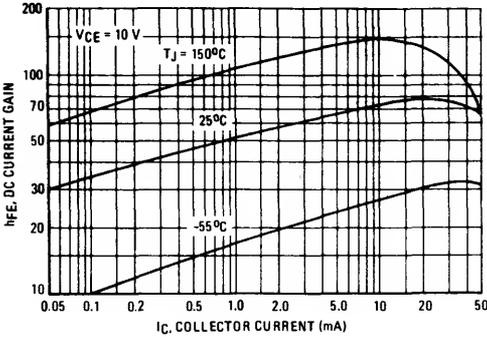
Characteristic	Symbol	Min	Max	Unit
<b>OFF CHARACTERISTICS</b>				
Collector-Emitter Breakdown Voltage(1) (I <sub>C</sub> = 10 mAdc, I <sub>B</sub> = 0)	V <sub>(BR)CEO</sub>	300	—	Vdc
Collector-Base Breakdown Voltage (I <sub>C</sub> = 100 μAdc, I <sub>E</sub> = 0)	V <sub>(BR)CBO</sub>	300	—	Vdc
Emitter-Base Breakdown Voltage (I <sub>E</sub> = 100 μAdc, I <sub>C</sub> = 0)	V <sub>(BR)EBO</sub>	7.0	—	Vdc
Collector Cutoff Current (V <sub>CB</sub> = 200 Vdc, I <sub>E</sub> = 0) (V <sub>CB</sub> = 200 Vdc, I <sub>E</sub> = 0, T <sub>A</sub> = 100°C)	I <sub>CBO</sub>	— —	0.2 20	μAdc
Emitter Cutoff Current (V <sub>EB</sub> = 6.0 Vdc, I <sub>C</sub> = 0)	I <sub>EBO</sub>	—	0.2	μAdc
<b>ON CHARACTERISTICS(2)</b>				
DC Current Gain (I <sub>C</sub> = 3.0 mAdc, V <sub>CE</sub> = 10 Vdc) (I <sub>C</sub> = 10 mAdc, V <sub>CE</sub> = 10 Vdc) (I <sub>C</sub> = 30 mAdc, V <sub>CE</sub> = 10 Vdc) (I <sub>C</sub> = 50 mAdc, V <sub>CE</sub> = 20 Vdc)	h <sub>FE</sub>	10 15 20 20	— — 200 —	—
Collector-Emitter Saturation Voltage (I <sub>C</sub> = 10 mAdc, I <sub>B</sub> = 1.0 mAdc) (I <sub>C</sub> = 30 mAdc, I <sub>B</sub> = 3.0 mAdc)	V <sub>CE(sat)</sub>	— —	0.75 1.0	Vdc
Base-Emitter Saturation Voltage (I <sub>C</sub> = 10 mAdc, I <sub>B</sub> = 1.0 mAdc) (I <sub>C</sub> = 30 mAdc, I <sub>B</sub> = 3.0 mAdc)	V <sub>BE(sat)</sub>	— —	1.0 1.2	Vdc
<b>SMALL-SIGNAL CHARACTERISTICS</b>				
Current-Gain — Bandwidth Product(3) (I <sub>C</sub> = 10 mAdc, V <sub>CE</sub> = 20 Vdc, f = 20 MHz)	f <sub>T</sub>	30	—	MHz
Output Capacitance (V <sub>CB</sub> = 10 Vdc, I <sub>E</sub> = 0, f = 100 kHz)	C <sub>obo</sub>	—	6.0	pF
Input Capacitance (V <sub>EB</sub> = 0.5 Vdc, I <sub>C</sub> = 0, f = 100 kHz)	C <sub>ibo</sub>	—	80	pF
Input Impedance (I <sub>C</sub> = 10 mAdc, V <sub>CE</sub> = 10 Vdc, f = 1.0 kHz)	h <sub>ie</sub>	—	2.0	k ohms
Voltage Feedback Ratio (I <sub>C</sub> = 10 mAdc, V <sub>CE</sub> = 10 Vdc, f = 1.0 kHz)	h <sub>re</sub>	—	2.0	X10 <sup>-4</sup>
Small-Signal Current Gain (I <sub>C</sub> = 10 mAdc, V <sub>CE</sub> = 10 Vdc, f = 10 kHz)	h <sub>fe</sub>	20	200	—

**ELECTRICAL CHARACTERISTICS** (continued) ( $T_A = 25^\circ\text{C}$  unless otherwise noted.)

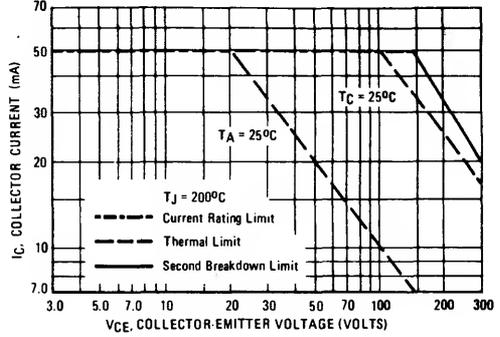
Characteristic	Symbol	Min	Max	Unit
Output Admittance ( $I_C = 10\text{ mAdc}$ , $V_{CE} = 10\text{ Vdc}$ , $f = 1.0\text{ kHz}$ )	$h_{oe}$	—	50	mhos
Real Part of Input Impedance ( $I_C = 10\text{ mAdc}$ , $V_{CE} = 10\text{ Vdc}$ , $f = 5.0\text{ MHz}$ )	$\text{Re}(h_{ie})$	—	200	Ohms

- (1) Pulse Test: Pulse Width  $\leq 30\ \mu\text{s}$ , Duty Cycle  $\leq 1.0\%$ .
- (2) Pulse Test: Pulse Width  $\leq 300\ \mu\text{s}$ , Duty Cycle  $\leq 2.0\%$ .
- (3)  $f_T$  is defined as the frequency at which  $|h_{fe}|$  extrapolates to unity.

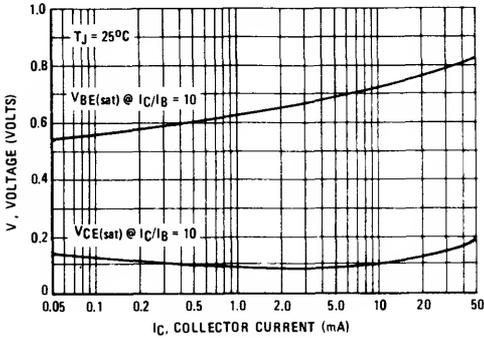
**FIGURE 1 – DC CURRENT GAIN**



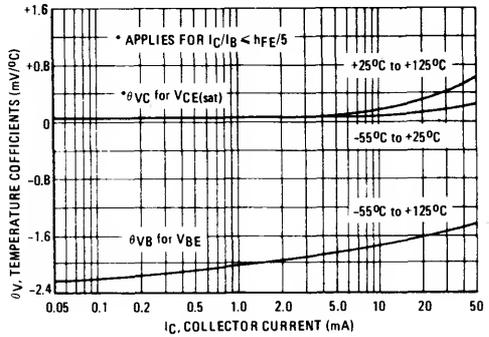
**FIGURE 2 – DC SAFE OPERATING AREA**



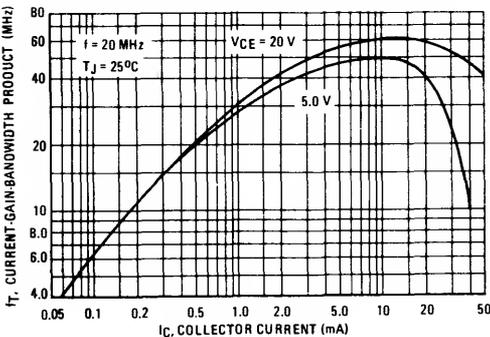
**FIGURE 3 – "ON" VOLTAGES**



**FIGURE 4 – TEMPERATURE COEFFICIENTS**



**FIGURE 5 – CURRENT-GAIN-BANDWIDTH PRODUCT**



**FIGURE 6 – CAPACITANCE**

