

# Preliminary GTRA360502M

Thermally-Enhanced High Power RF GaN on SiC HEMT 50 W, 48 V, 3400 – 3800 MHz

#### Description

The GTRA360502M is a 50-watt (P3dB) GaN on Sic high electron mobility transistor (HEMT) for use in multi-standard cellular power amplifier applications. It features input matching, high efficiency, and a thermallyenhanced, overmold package.

#### Features

- GaN on SiC HEMT technology
- Asymmetric Doherty design - Main: P1dB = 20 W Typ
  - Peak: P1dB = 37 W Typ
- Typical pulsed CW performance, 3500 MHz, 48 V - Output power at P<sub>3dB</sub> = 50 W
  - Gain = 15 dB
  - Efficiency = 55%
- · Low thermal resistance



Preliminary Data Sheets describe products that are being considered

by Wolfspeed for development and

market introduction. The target

performance shown in Preliminary

Data Sheets is not final and should not be used for any design activity. Please contact Wolfspeed about the future availability of these products.

Package: PG-DFN-6.5x7-1

#### **Target RF Characteristics**

#### Single-carrier WCDMA Specifications (tested in Wolfspeed test fixture)

V<sub>DD</sub> = 48 V, I<sub>DQ</sub> = 26 mA, P<sub>OUT</sub> = 7 W avg, V<sub>GS(PEAK)</sub> = VGS @ I<sub>DQ</sub> = 43 mA - 2.5 V, f = 3600 MHz, 3GPP, channel bandwidth = 3.84 MHz, peak/average = 10 dB @ 0.01% CCDF

Characteristic	Symbol	Min	Тур	Мах	Unit
Gain	G <sub>ps</sub>	_	15	_	dB
Drain Efficiency	ηD	_	55	_	%
Adjacent Channel Power Ratio	ACPR	_	-22	—	dBc

All published data at T<sub>CASE</sub> = 25°C unless otherwise indicated ESD: Electrostatic discharge sensitive device-observe handling precautions!



#### **DC Characteristics**

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Characteristic	Symbol	Min	Тур	Мах	Unit	
Drain-source Breakdown Voltage (main) $V_{GS}$ = –8 V, I <sub>D</sub> = 0.36 mA		V <sub>(BR)DSS</sub>	150	_	_	V
	(peak) $V_{GS}$ = –8 V, I <sub>D</sub> = 0.60 mA	V <sub>(BR)DSS</sub>	150	—	—	V
Drain-source Leakage Current $V_{GS} = -8 V$ , $V_{DS} = 10 V$		I <sub>DSS</sub>	_	_	5	mA
Gate Threshold Voltage (main) $V_{DS} = 10 \text{ V}$ , $I_D = 26 \text{ mA}$		V <sub>GS(th)</sub>	_	-3	_	V
	(peak) V <sub>DS</sub> = 10 V, I <sub>D</sub> = 43 mA	V <sub>GS(th)</sub>	_	-3	_	V

#### **Recommended Operating Conditions**

Parameter	Symbol	Min	Тур	Мах	Unit	
Operating Voltage		V <sub>DD</sub>	0	_	50	V
Gate Quiescent Voltage	$V_{DS}$ = 48 V, $I_D$ = TBD mA	V <sub>GS(Q)</sub>	_	TBD	_	V

#### **Absolute Maximum Ratings**

Parameter		Symbol	Value	Unit
Drain-source Voltage		V <sub>DSS</sub>	125	V
Gate-source Voltage		V <sub>GS</sub>	-10 to +2	V
Operating Voltage		V <sub>DD</sub>	55	V
Gate Current	(main)	IG	2.5	mA
	(peak)	IG	4.3	mA
Drain Current	(main)	Ι <sub>D</sub>	0.97	А
	(peak)	۱ <sub>D</sub>	1.6	А
Junction Temperature		Tj	225	°C
Storage Temperature Range		T <sub>STG</sub>	-65 to +150	°C

Operation above the maximum values listed here may cause permanent damage. Maximum ratings are absolute ratings; exceeding only one of these values may cause irreversible damage to the component. Exposure to absolute maximum rating conditions for extended periods may affect device reliability. For reliable continuous operation, the device should be operated within the operating voltage range (V<sub>DD</sub>) specified above.

#### **Thermal Chracteristics**

Thermal resistance, junction to case (T<sub>FLANGE</sub> = 70°C)

Parameter		Symbol	Value	Unit
Thermal Resistance	Main:	R <sub>θJC</sub>	TBD	°C/W
	Peak:	$R_{\theta JC}$	TBD	°C/W

#### **Moisture Sensitivity Level**

Level	Test Signal	Package Temperature	Unit
3	IPC/JEDEC J-STD-020	260	°C



## **Ordering Information**

Type and Version	Order Code	Package	Shipping
GTRA360502M V1 (TBD)	TBD	PG-DFN-6.5x7-1, overmold	TBD

#### Pinout Diagram (bottom view)



\* It is recommended that all pins labelled "NC" be connected to ground



# **Package Outline Specifications**





### **Revision History**

Revision	Date	Data Sheet Type	Page	Subjects (major changes since last revision)
01	2020-07-16	Preliminary	All	Data Sheet reflects preliminary specification for product development

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