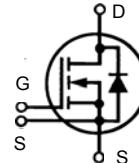


HiPerRF™ Power MOSFETs F-Class: MegaHertz Switching

N-Channel Enhancement Mode
Avalanche Rated, Low Q_g , Low Intrinsic R_g
High dV/dt, Low t_{rr}

IXFN 55N50F

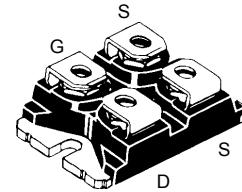


$V_{DSS} = 500 \text{ V}$
 $I_{D25} = 55 \text{ A}$
 $R_{DS(on)} = 85 \text{ m}\Omega$

$t_{rr} \leq 250 \text{ ns}$

miniBLOC, SOT-227B

E153432



G = Gate D = Drain
S = Source

Either Source terminal at miniBLOC can be used as Main or Kelvin Source

Symbol	Test Conditions	Maximum Ratings		
V_{DSS}	$T_J = 25^\circ\text{C}$ to 150°C	500	V	
V_{DGR}	$T_J = 25^\circ\text{C}$ to 150°C ; $R_{GS} = 1 \text{ M}\Omega$	500	V	
V_{GS}	Continuous	± 20	V	
V_{GSM}	Transient	± 30	V	
I_{D25}	$T_c = 25^\circ\text{C}$	55	A	
I_{DM}	$T_c = 25^\circ\text{C}$, pulse width limited by T_{JM}	220	A	
I_{AR}	$T_c = 25^\circ\text{C}$	55	A	
E_{AR}	$T_c = 25^\circ\text{C}$	60	mJ	
E_{AS}	$T_c = 25^\circ\text{C}$	3.0	J	
dv/dt	$I_s \leq I_{DM}$, $dI/dt \leq 100 \text{ A}/\mu\text{s}$, $V_{DD} \leq V_{DSS}$, $T_J \leq 150^\circ\text{C}$, $R_G = 2 \Omega$	10	V/ns	
P_D	$T_c = 25^\circ\text{C}$	600	W	
T_J		-55 ... +150	$^\circ\text{C}$	
T_{JM}		150	$^\circ\text{C}$	
T_{stg}		-55 ... +150	$^\circ\text{C}$	
T_J	1.6 mm (0.63 in) from case for 10 s	-	$^\circ\text{C}$	
V_{ISOL}	50/60 Hz, RMS $t = 1 \text{ min}$ $I_{ISOL} \leq 1 \text{ mA}$ $t = 1 \text{ s}$	2500 3000	V~ V~	
M_d	Mounting torque Terminal connection torque	1.5/13 1.5/13	Nm/lb.in. Nm/lb.in.	
Weight		30	g	

Symbol	Test Conditions	Characteristic Values		
		($T_J = 25^\circ\text{C}$, unless otherwise specified)		
		min.	typ.	max.
V_{DSS}	$V_{GS} = 0 \text{ V}$, $I_D = 1 \text{ mA}$ BV_{DSS} Temperature Dependence	500	0.53	V/K
$V_{GH(th)}$	$V_{DS} = V_{GS}$, $I_D = 8 \text{ mA}$ $V_{GS(th)}$ Temperature Dependence	3.0 -0.011	5.5 V/K	V/K
I_{GSS}	$V_{GS} = \pm 20 \text{ V}_{DC}$, $V_{DS} = 0$		± 200	nA
I_{DSS}	$V_{DS} = V_{DSS}$ $V_{GS} = 0 \text{ V}$	$T_J = 25^\circ\text{C}$ $T_J = 125^\circ\text{C}$	100 3	μA mA
$R_{DS(on)}$	$V_{GS} = 10 \text{ V}$, $I_D = 0.5 \cdot I_{D25}$ Pulse test, $t \leq 300 \mu\text{s}$, duty cycle $d \leq 2 \%$		85	$\text{m}\Omega$

Features

- RF capable Mosfets
- Rugged polysilicon gate cell structure
- Double metal process for low gate resistance
- Unclamped Inductive Switching (UIS) rated
- Low package inductance - easy to drive and to protect
- Fast intrinsicrectifier

Applications

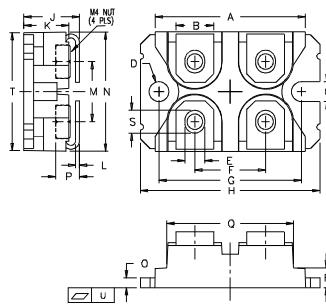
- DC-DC converters
- Switched-mode and resonant-mode power supplies, >500kHz switching
- DC choppers
- Pulse generation
- Laser drivers

Advantages

- Easy to mount
- Space savings
- High power density

Symbol	Test Conditions	Characteristic Values			
		(T _J = 25°C, unless otherwise specified)	min.	typ.	max.
g_{fs}	V _{DS} = 10 V; I _D = 0.5 • I _{D25} ; pulse test	22	33	S	
C _{iss}	V _{GS} = 0 V, V _{DS} = 25 V, f = 1 MHz	6700		pF	
C _{oss}		1250		pF	
C _{rss}		330		pF	
t _{d(on)}	V _{GS} = 10 V, V _{DS} = 0.5 • V _{DSS} , I _D = 0.5 • I _{D25} R _G = 1 Ω (External),	24		ns	
t _r		20		ns	
t _{d(off)}		45		ns	
t _f		9.6		ns	
Q _{G(on)}	V _{GS} = 10 V, V _{DS} = 0.5 • V _{DSS} , I _D = 0.5 • I _{D25}	195		nC	
Q _{GS}		50		nC	
Q _{GD}		95		nC	
R _{thJC}			0.21	K/W	
R _{thCK}			0.05	K/W	

miniBLOC, SOT-227 B



M4 screws (4x) supplied

Dim.	Millimeter		Inches	
	Min.	Max.	Min.	Max.
A	31.50	31.88	1.240	1.255
B	7.80	8.20	0.307	0.323
C	4.09	4.29	0.161	0.169
D	4.09	4.29	0.161	0.169
E	4.09	4.29	0.161	0.169
F	14.91	15.11	0.587	0.595
G	30.12	30.30	1.186	1.193
H	38.00	38.23	1.496	1.505
J	11.68	12.22	0.460	0.481
K	8.92	9.60	0.351	0.378
L	0.76	0.84	0.030	0.033
M	12.60	12.85	0.496	0.506
N	25.15	25.42	0.990	1.001
O	1.98	2.13	0.078	0.084
P	4.95	5.97	0.195	0.235
Q	26.54	26.90	1.045	1.059
R	3.94	4.42	0.155	0.174
S	4.72	4.85	0.186	0.191
T	24.59	25.07	0.968	0.987
U	-0.05	0.1	-0.002	0.004

Source-Drain Diode

Characteristic Values

(T_J = 25°C, unless otherwise specified)

Symbol	Test Conditions	min.	typ.	max.
I _s	V _{GS} = 0 V		55	A
I _{SM}	Repetitive; pulse width limited by T _{IM}		220	A
V _{SD}	I _F = I _S , V _{GS} = 0 V, Pulse test, t ≤ 300 μs, duty cycle d ≤ 2 %		1.5	V
t _{rr}	I _F = 25 A, -di/dt = 100 A/μs, V _R = 100 V	1.0	250	ns
Q _{RM}		10		μC
I _{RM}				A

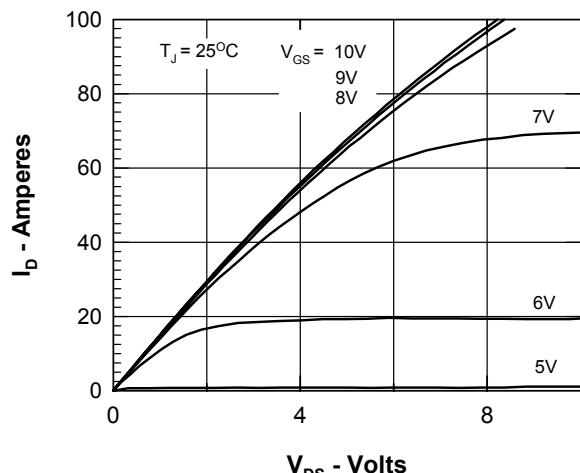


Figure 1. Output Characteristics at 25°C

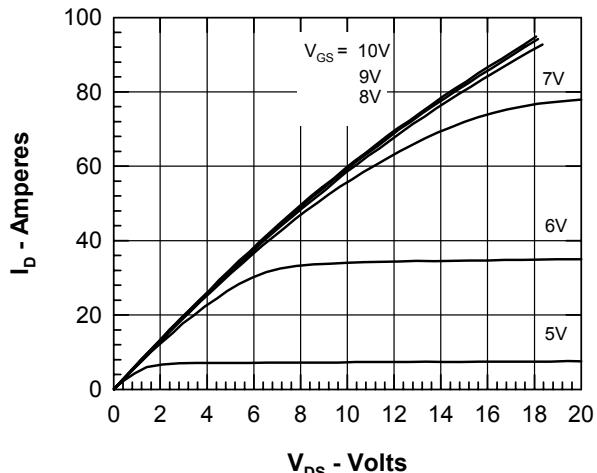


Figure 2. Output Characteristics at 125°C

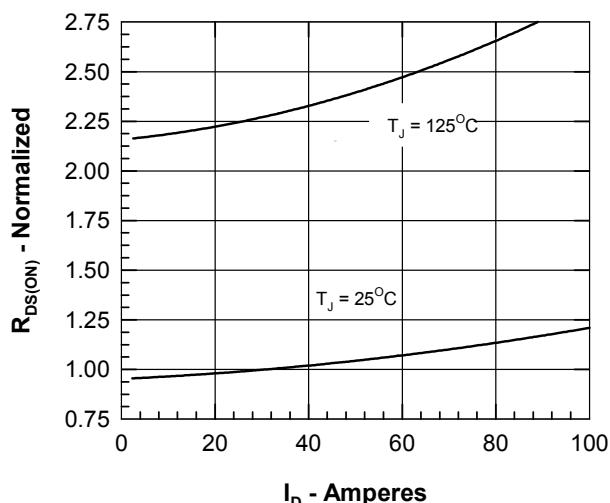


Figure 3. $R_{DS(on)}$ normalized to $0.5 I_{D25}$ value vs. I_D

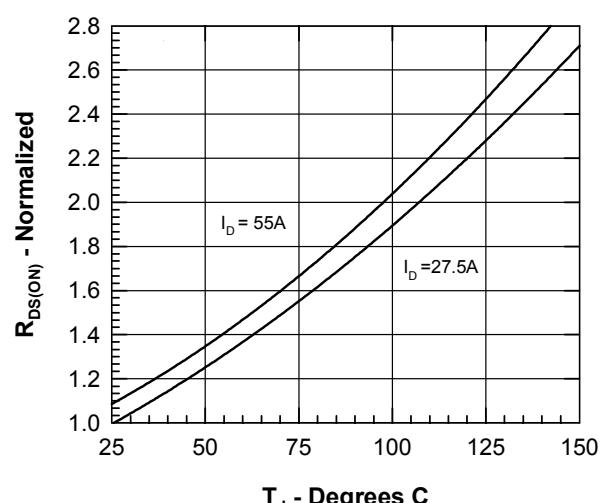


Figure 4. $R_{DS(on)}$ normalized to $0.5 I_{D25}$ value vs. T_J

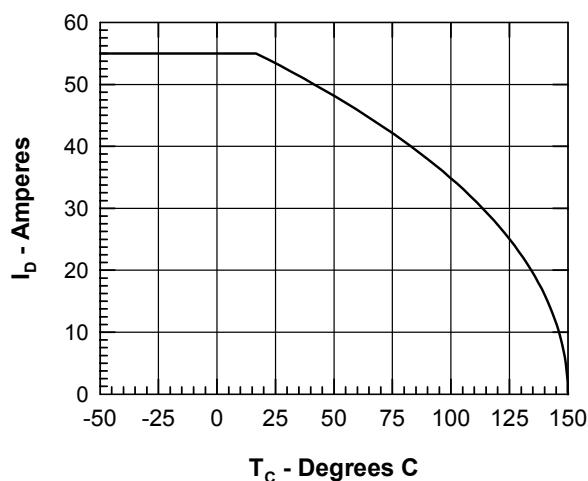


Figure 5. Drain Current vs. Case Temperature

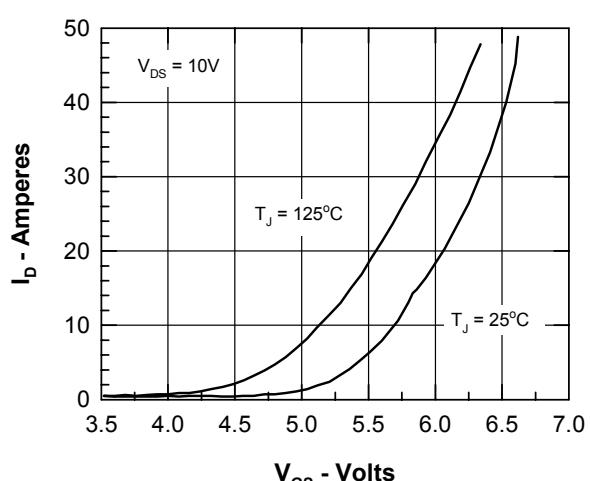


Figure 6. Admittance Curves

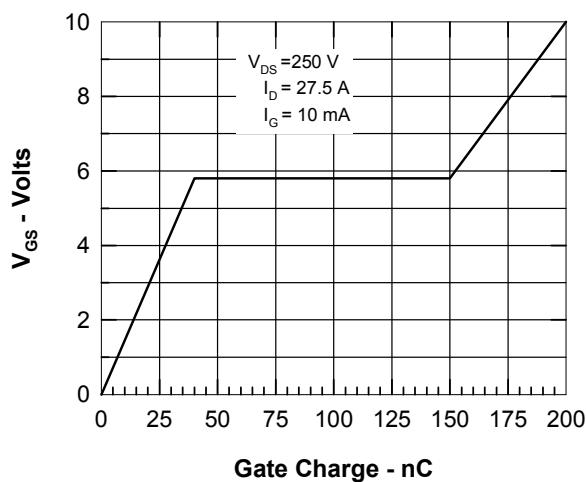


Figure 7. Gate Charge

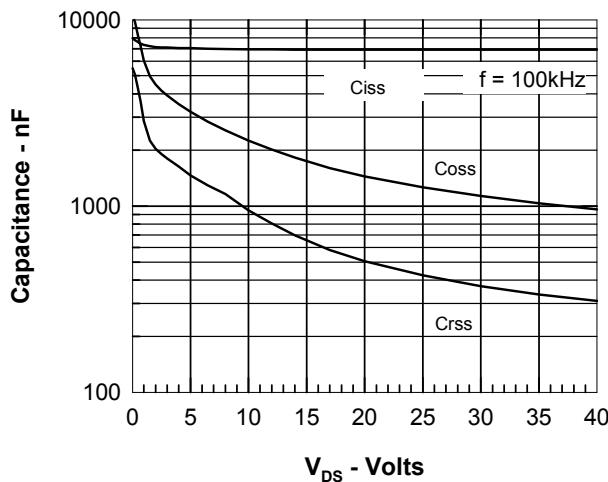


Figure 8. Capacitance Curves

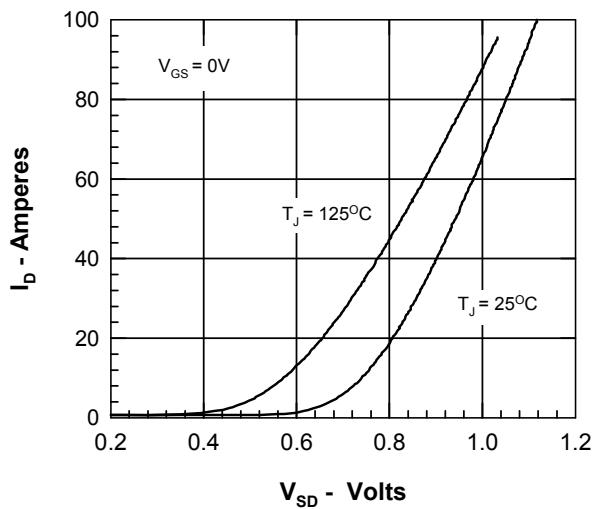


Figure 9. Forward Voltage Drop of the Intrinsic Diode

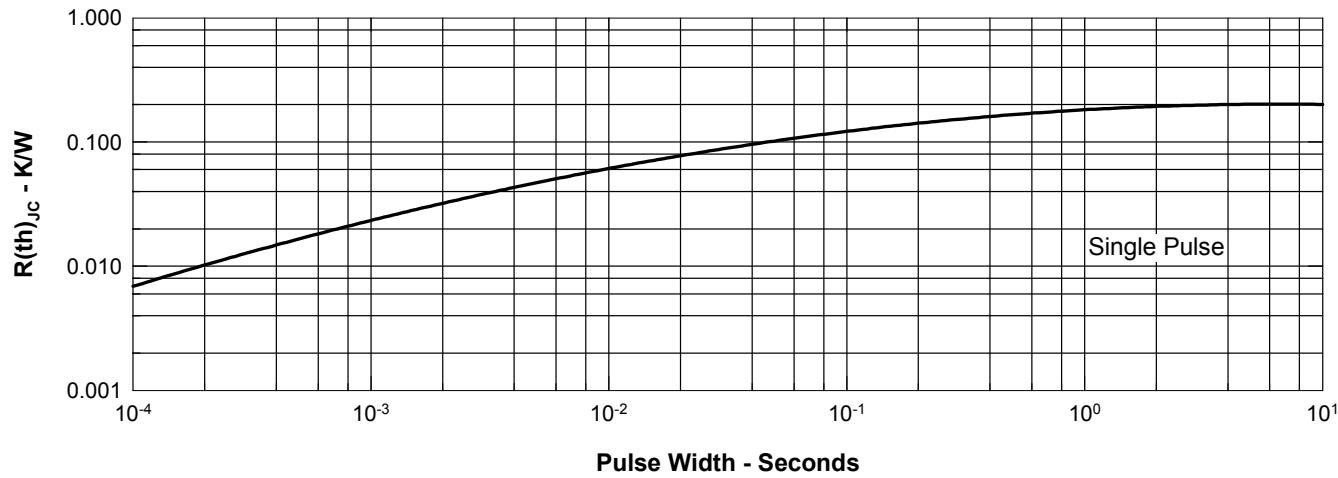


Figure 10. Transient Thermal Resistance

IXYS reserves the right to change limits, test conditions, and dimensions.

IXYS MOSFETs and IGBTs are covered by one or more of the following U.S. patents:

4,835,592	4,881,106	5,017,508	5,049,961	5,187,117	5,486,715	6,306,728B1
4,850,072	4,931,844	5,034,796	5,063,307	5,237,481	5,381,025	