

# BIPOLAR ANALOG INTEGRATED CIRCUIT µPC1316

# **DUAL AUDIO POWER AMPLIFIER**

DATA SHEET

#### DESCRIPTION

The  $\mu$ PC1316 is a dual audio power amplifier designed for portable audio sets.

## FEATURES

- Wide operating voltage range. V<sub>CC</sub> = 3 to 16 V
- High output power.  $P_0 = 2$  W TYP. @  $12 V / 8 \Omega / 10 \%$ 
  - $P_{O} = 1.6 \text{ W TYP.} @ 9 \text{ V} / 4 \Omega / 10 \%$   $P_{O} = 1.2 \text{ W TYP.} @ 9 \text{ V} / 8 \Omega / 10 \%$   $P_{O} = 0.7 \text{ W TYP.} @ 6 \text{ V} / 4 \Omega / 10 \%$   $P_{O} = 0.5 \text{ W TYP.} @ 6 \text{ V} / 8 \Omega / 10 \%$   $P_{O} = 80 \text{ mW} @ 4.5 \text{ V} / 32 \Omega / 10 \%$

- High supply voltage rejection. SVR = 45 dB
- Low quiescent current. I<sub>CC</sub> = 12 mA
- Low pop noise at power switch on and off.

#### **BLOCK DIAGRAM**



#### CONNECTION DIAGRAM

PIN NO	CONNECTION		
1	Filter		
2	Input 2		
3	NFB 2		
4	Compensation 2		
5	Bootstrap 2		
6	Output 2		
7	NC		
ТАВ	GND		
8	Output 1		
9	Bootstrap 1		
10	V <sub>CC</sub>		
11	Compensation 1		
12	NFB 1		
13	Input 1		
14	GND		

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## **ORDERING INFORMATION**

PART NUMBER	PACKAGE	QUALITY GRADE
μPC1316C	14 PIN PLASTIC DIP WITH TAB (300 mil)	Standard

Please refer to "Quality grade on NEC Semiconductor Devices" (Document number IEI-1209) published by NEC Corporation to know the specification of quality grade on the devices and its recommended applications.

## ABSOLUTE MAXIMUM RATINGS (T<sub>a</sub> = 25 °C)

Supply Voltage (No Signal)	V <sub>CC1</sub>	18	V
Supply Voltage (Operating)	V <sub>CC2</sub>	16	V
Power Dissipation	PD	2.4 *	W
<sup>e</sup> Operating Temperature	T <sub>opt</sub>	20 to +70	°C
Storage Temperature	T <sub>stg</sub>	-40 to +150	°C

\*  $50 \times 50 \times 0.035$  mm Copper heat sink on PCB

## RECOMMENDED OPERATING CONDITIONS (T<sub>a</sub> = 25 °C)

CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT
Supply Voltage (RL=16 $\Omega$ )	V <sub>CC</sub> (16)	3		16	v
Supply Voltage (RL=8 $\Omega$ )	V <sub>CC</sub> (8)	3		13	v
Supply Voltage (RL=4 $\Omega$ )	V <sub>CC</sub> (4)	3		9	v
Load Impedance	RL	4	8		Ω
Voltage Gain	A <sub>v</sub>	34	44		dB

## ELECTRICAL CHARACTERISTICS (T<sub>a</sub> = 25 °C) (V<sub>CC</sub>=9 V, R<sub>f</sub>=33 $\Omega$ , f=1 kHz, R<sub>L</sub>=8 $\Omega$ )

CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT	CONDITION
Circuit Current	ICC		12	25	mA	No Signal
Voltage Gain	A <sub>v1</sub>	41	44	47	dB	P <sub>O</sub> =0.25 W, R <sub>f</sub> =33 Ω
	A <sub>v2</sub>		34		dB	$P_{O}$ =0.25 W, R <sub>f</sub> =120 Ω
Output Power	PO1		2		w	V <sub>CC</sub> =12 V, R <sub>L</sub> =8 Ω, THD = 10 %
	P <sub>O2</sub>		1.6		w	$V_{CC}$ =9 V, RL=4 $\Omega$ , THD = 10 %
	P <sub>O3</sub>	0.9	1.2		w	V <sub>CC</sub> =9 V, R <sub>L</sub> =8 Ω, THD = 10 %
	P <sub>O4</sub>		0.7		w	V <sub>CC</sub> =6 V, R <sub>L</sub> =4 Ω, THD = 10 %
	P <sub>O5</sub>		0.5		w	V <sub>CC</sub> =6 V, R <sub>L</sub> =8 Ω, THD = 10 %
	P <sub>O6</sub>		80		mW	$V_{CC}$ =4.5 V, RL=32 $\Omega$ , THD = 10 %
	THD1		0.4	1.6	%	P <sub>O</sub> =0.5 W, R <sub>f</sub> =33 Ω
Total Harmonic Distortion	THD2		0.3		%	P <sub>O</sub> =0.5 W, R <sub>f</sub> =120 Ω
Output Noise Voltage	NL	-	0.9	1.5	mVr.m.s.	R <sub>G</sub> =10 kΩ
Supply Voltage Rejection	SVR	36	45		dB	R <sub>G</sub> =0, f(ripple)=100 Hz, V(ripple)=0.3 V <sub>r.m.s.</sub>
Cross Talk	СТ	40	55		dB	R <sub>G</sub> =0, P <sub>O</sub> =0.25 W
Channel Balance	ChB	-2	0	2	dB	P <sub>O</sub> =0.25 W
Input Impedance	Z <sub>in</sub>		5		MΩ	

**TEST CIRCUIT** 

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# μ**PC1316**



#### NOTE FOR USE

- (1) Mylar capacitor is recommended as  $C_{12}$ ,  $C_{13}$ .
- (2) Add  $C_7$ ,  $C_9$ , in the case of reducing voltage gain at high frequency.
- (3) Add C<sub>4</sub> or increase capacitance of  $C_{12}$ ,  $C_{13}$  when a oscillation may occur due to the pattern layout on PCB.
- (4) Voltage gain can be changed by value of  $R_{f1}$ ,  $R_{f2}$ , The voltage gain should be set more than 34 dB.
- (5) When a input capacitor is connected the input terminal, a bias resistor should be connected between its terminal and GND.

### EXAMPLE FOR PRINTED CIRCUIT BOARD (Copper foil side)



## μPC1316

## TYPICAL CHARACTERISTICS (T<sub>a</sub> = 25 $^{\circ}$ C)

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VOLTAGE GAIN, TOTAL HARMONIC DISTORTION vs. R<sub>f</sub>



MAXIMUM POWER DISSIPATION vs. SUPPLY VOLTAGE



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0.3 0.5

Po-Output Power-W

1

3 5 10

PACKAGE DISSIPATION vs. AMBIENT TEMPERATURE Fig.A 3 Fig.A l=45 50×50×0.035 mm mm 2.5 Copper on PCB Fig.A £=25 mm Infinite Heatsink PD-Package Dissipation-W ∞⊺ 2 5 35×35×0.035 Fig.A mm \_\_1 1.5 Copper on PCB UNIT : mm 1 Free Air 0.5 0 25 75 100 125 150 50 Ta-Ambient Temperature-°C

0.01

0.030.05 0.1

14PIN PLASTIC DIP WITH TAB (300 mil)



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N N 6





P14CT-100-300B

## NOTES

- Each lead centerline is located within 0.25 mm (0.01 inch) of its true position (T.P.) at maximum material condition.
- Item "K" to center of leads when formed parallel.

ITEM	MILLIMETERS	INCHES
А	20.32 MAX.	0.800 MAX.
A'	24.60 MAX.	0.969 MAX.
В	2.54 MAX.	0.100 MAX.
С	2.54 (T.P.)	0.100 (T.P.)
C′	4.74	0.187
D	0.50 = 0.10	0.020+0.004
F	1.1 MIN.	0.043 MIN.
G	3.4 <sup>±0.3</sup>	0.134 <sup>±0.012</sup>
Н	0.51 MIN.	0.020 MIN.
I	4.31 MAX.	0.170 MAX.
J	5.08 MAX.	0.200 MAX.
к	7.62 (T.P.)	0.300 (T.P.)
L	6.4	0.252
м	0.30 - 0.05	0.012 -0.003
N	0.25	0.01
٥	4.40 <sup>±0.50</sup>	0.173 <sup>±0.020</sup>

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