

DATA SHEET

GENERAL PURPOSE CHIP RESISTORS

RC0603 (Pb Free) 5%, 1%





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Chip Resistor Surface Mount SERIES 0603 (Pb Free)

SCOPE

This specification describes RC0603 series chip resistors with lead-free terminations made by thick film process.

ORDERING INFORMATION

Part number is identified by the series, size, tolerance, packing type, temperature coefficient, taping reel and resistance value.

PHYCOMP ORDERING CODE

I2NC CODE

2322 / 2350 XXX XXXXX L **(I)** (2) (3) (4)

	START	TOL.	RESISTANCE	PAPER	R / PE TAPE ON REE	L (units) ⁽²⁾
0603	IN ^(I)	(%)	RANGE	5,000	10,000/not preferred	20,000
RC21	2322	±5%	I to I0 $M\Omega$	702 60xxx	702 70xxx	702 81xxx
RC22	2322	±1%	I to I0 $M\Omega$	704 6xxx	704 7xxx	704 8xxx
HRC21	2350	±5%	II to 22 $M\Omega$	522 10xxx	-	-
Jumper	2322	-	0 Ω	702 96001	702 9700 I	702 92002

- (1) The resistors have a 12-digit ordering code starting with 2322.
- (2) The subsequent 4 or 5 digits indicate the resistor tolerance and packaging.
- (3) The remaining 4 or 3 digits represent the resistance value with the last digit indicating the multiplier as shown in the table of "Last digit of 12NC".
- (4) "L" means lead-free terminations.

ORDERING EXAMPLE

The ordering code of a RC22 resistor, value 56 Ω with $\pm 1\%$ tolerance, supplied in tape of 5,000 units per reel is: 232270465609L.

Last digit of I2NC	
Resistance decade (3)	Last digit
0.01 to 0.0976 Ω	0
0.1 to 0.976 Ω	7
I to 9.76 Ω	8
10 to 97.6 Ω	9
100 to 976 Ω	1
I to 9.76 $k\Omega$	2
10 to 97.6 kΩ	3
100 to 976 $k\Omega$	4
I to 9.76 $M\Omega$	5
10 to 97.6 MΩ	6

Example:	0.02 Ω	=	0200 or 200
	0.3 Ω	=	3007 or 307
	ΙΩ	=	1008 or 108
	33 kΩ	=	3303 or 333
	10 MΩ	=	1006 or 106

CTC CODE

RC0603	<u>X</u>	<u>X</u>	<u>X</u>	XX	<u>XXXX</u>	L
	(1)	(2)	(3)	(4)	(5)	(6)

(3) TEMPERATURE COEFFICIENT OF RESISTANCE

- = Base on spec

(4) TAPING REEL

07 = 7 inch dia. Reel
10 = 10 inch dia. Reel (not preferred)
13 = 13 inch dia. Reel

(5) RESISTANCE VALUE

5R6, 56R, 560R, 5K6, 56K, 22M

(6) RESISTOR TERMINATIONS

L = Lead free terminations (pure Tin)

ORDERING EXAMPLE

The ordering code of a RC0603 chip resistor, value 56 Ω with ±1% tolerance, supplied in 7-inch tape reel is: RC0603FR-0756RL.

NOTE

- 1. The "L" at the end of the code is only for ordering. On the reel label, the standard CTC or I2NC will be mentioned an additional stamp "LFP"= lead free production.
- 2. Products with lead in terminations fulfil the same requirements as mentioned in this datasheet.
- 3. Products with lead in terminations will be phased out in the coming months (before July 1st, 2006)



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MARKING

RC0603



E-24 series: 3 digits

First two digits for significant figure and 3rd digit for number of zeros





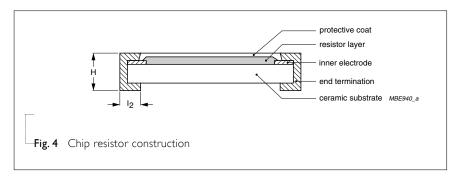
E-96 series: 3 digits for 0603 $\pm 1\%$ EIA-96 marking method

For 0603 ±1% E-24 series, one short bar under marking letter

For marking codes, please see EIA-marking code rules in data sheet "Chip resistors instruction".

CONSTRUCTION

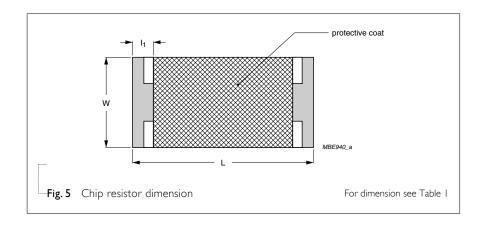
The resistors are constructed out of a high-grade ceramic body. Internal metal electrodes are added at each end and connected by a resistive paste. The composition of the paste is adjusted to give the approximate required resistance and laser cutting of this resistive layer that achieves tolerance trims the value. The resistive layer is covered with a protective coat and printed with the



resistance value. Finally, the two external terminations (pure Tin) are added. See fig. 4.

DIMENSIONS

RC0603
1.60 ±0.1
0.80 ±0.10
0.45 ±0.10
0.25 ±0.15
0.25 ±0.15



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ELECTRICAL CHARACTERISTICS

Table 2		
CHARACTERISTICS	R	C0603 1/10 W
Operating Temperature Range	– 55	°C to +155 °C
Maximum Working Voltage		50 V
Maximum Overload Voltage		100 V
Dielectric Withstanding Voltage		100 V
	5% (E24)	I Ω to 22 MΩ
Resistance Range	1% (E96)	I Ω to I0 M Ω
	Zero Ohm Ju	ımper < 0.05 Ω
Temperature Coefficient	$10 \Omega < R \le 10 M\Omega$	±100 ppm/°C
Temperature Coemcient	$R \le 10 \Omega$; $R > 10 M\Omega$	±200 ppm/°C
Jumper Criteria	Rated Current	I.0 A
	Maximum Current	2.0 A

FOOTPRINT AND SOLDERING PROFILES

For recommended footprint and soldering profiles, please see the special data sheet "Chip resistors mounting".

ENVIRONMENTAL DATA

For material declaration information (IMDS-data) of the products, please see the separated info "Environmental data".

PACKING STYLE AND PACKAGING QUANTITY

Table 3 Packing style and packaging quantity

PRODUCT TYPE	PACKING STYLE	REEL DIMENSION	QUANTITY PER REEL
RC0603	Paper / PE Taping Reel (R)	7" (178 mm)	5,000 units
		10" (254 mm) / not preferred	10,000 units
		13" (330 mm)	20,000 units

NOTE

1. For Paper/PE tape and reel specification/dimensions, please see the special data sheet "Packing" document.



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FUNCTIONAL DESCRIPTION

POWER RATING

RC0603 rated power at 70°C is I/I0 W

RATED VOLTAGE

The DC or AC (rms) continuous working voltage corresponding to the rated power is determined by the following formula:

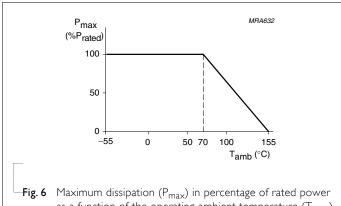
$$V=\sqrt{(P \times R)}$$

Where

V=Continuous rated DC or AC (rms) working voltage (V)

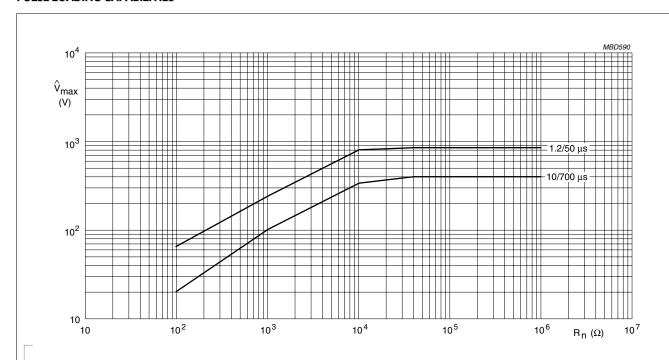
P=Rated power (W)

R=Resistance value (Ω)



as a function of the operating ambient temperature (T_{amb})

PULSE LOADING CAPABILITIES



Maximum permissible peak pulse voltage without failing to open circuit' in accordance with DIN IEC 60040 (CO) 533 for type: RC0603

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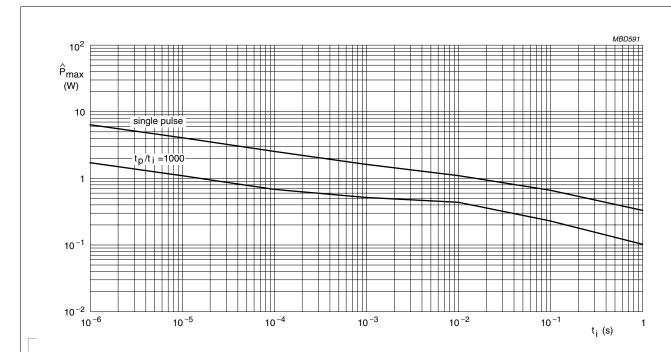


Fig. 8 Pulse on a regular basis for type: RC0603; maximum permissible peak pulse power as a function of pulse duration for single pulse and repetitive pulse tp/ti = 1000

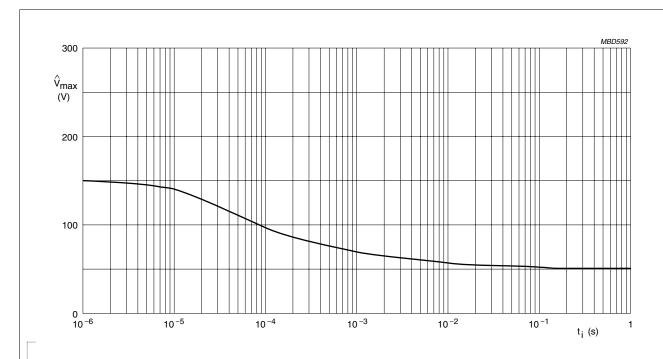


Fig. 9 Pulse on a regular basis for type: RC0603; maximum permissible peak pulse voltage as a function of pulse duration

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TESTS AND REQUIREMENTS

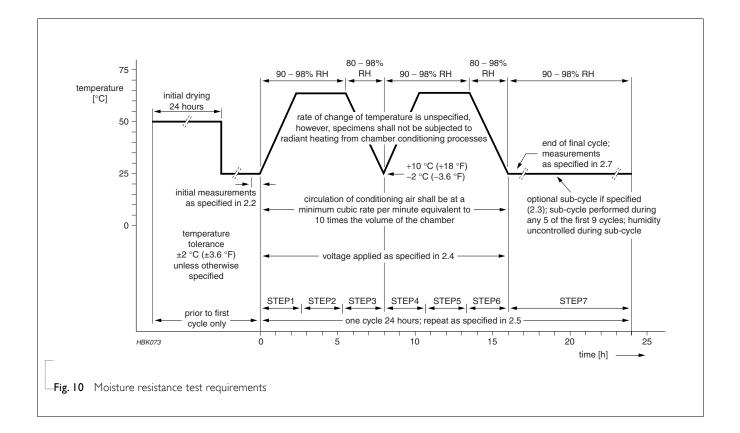
Table 4 Test condition, procedure and requirements

TEST	TEST METHOD	PROCEDURE	REQUIREMENTS
Temperature Coefficient of	MIL-STD-202F-method 304; JIS C 5202-4.8	At +25/–55 °C and +25/+125 °C	Refer to table 2
Resistance	JIS C 3202-1.0	Formula:	
(T.C.R.)		T.C.R= $\frac{R_2-R_1}{R_1(t_2-t_1)} \times 10^6 \text{ (ppm/°C)}$	
		Where t_1 =+25 °C or specified room temperature	
		t_2 =-55 °C or +125 °C test temperature	
		R_1 =resistance at reference temperature in ohms	
		R ₂ =resistance at test temperature in ohms	
Thermal Shock	MIL-STD-202F-method 107G;	At -65 (+0/-10) °C for 2 minutes and at +155	$\pm (0.5\% + 0.05~\Omega)$ for 1% tol.
	IEC 60115-1 4.19	(+10/-0) °C for 2 minutes; 25 cycles	\pm (1.0%+0.05 Ω) for 5% tol.
Low	MIL-R-55342D-Para 4.7.4	At -65 (+0/-5) °C for I hour, RCWV applied	$\pm (0.5\% + 0.05~\Omega)$ for 1% tol .
Temperature		for 45 (+5/–0) minutes	$\pm (1.0\% {+} 0.05~\Omega)$ for 5% tol.
Operation			No visible damage
Short Time	MIL-R-55342D-Para 4.7.5;	2.5 × RCWV applied for 5 seconds at room	\pm (1.0%+0.05 Ω) for 1% tol.
Overload	IEC 60115-1 4.13	temperature	$\pm (2.0\% + 0.05~\Omega)$ for 5% tol.
			No visible damage
Insulation	MIL-STD-202F-method 302;	RCOV for I minute	≥10 GΩ
Resistance	IEC 60115-1 4.6.1.1	Type RC0603	
		Voltage (DC)	
Dielectric	MIL-STD-202F-method 301;	Maximum voltage (V _{ms}) applied for 1 minute	No breakdown or flashover
Withstand	IEC 60115-1 4.6.1.1	Type RC0603	
Voltage		Voltage (AC) 100 V _{rms}	
		Too V _{rms}	
Resistance to	MIL-STD-202F-method 210C;	Unmounted chips; 260 \pm 5 °C for 10 \pm 1	$\pm (0.5\% + 0.05~\Omega)$ for 1% tol.
Soldering	IEC 60115-1 4.18	seconds	\pm (1.0%+0.05 Ω) for 5% tol.
Heat			No visible damage
Life	MIL-STD-202F-method 108A;	At 70±2 °C for 1,000 hours; RCWV applied for	\pm (1%+0.05 Ω) for 1% tol.
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ST	TEST METHOD	PROCEDURE	REQUIREMENTS		
Solderability	MIL-STD-202F-method 208A;	Solder bath at 245±3 °C	Well tinned (≥95% cove	ered)	
	IEC 60115-1 4.17	Dipping time: 2±0.5 seconds	No visible damage		
Bending	JIS C 5202.6.14;	Resistors mounted on a 90 mm glass epoxy	\pm (1.0%+0.05 Ω) for 1%	ś tol.	
Strength	IEC 60115-1 4.15	resin PCB (FR4)	$\pm (1.0\% + 0.05 \ \Omega)$ for 5% tol.		
		Bending: 5 mm	No visible damage		
Resistance to	MIL-STD-202F-method 215;	Isopropylalcohol (C ₃ H ₇ OH) or dichloromethane	No smeared		
Solvent	IEC 60115-1 4.29	(CH ₂ Cl ₂) followed by brushing			
Noise	JIS C 5202 5.9;	Maximum voltage (V _{ms}) applied.	Resistors range	Value	
	IEC 60115-1 4.12		R < 100 Ω	10 dB	
			$100 \Omega \le R < 1 K\Omega$	20 dB	
			$1 \text{ K}\Omega \leq R < 10 \text{ K}\Omega$	30 dB	
			$10 \text{ K}\Omega \leq R < 100 \text{ K}\Omega$	40 dB	
			$100 \text{ K}\Omega \leq R < 1 \text{ M}\Omega$	46 dB	
			$1 \text{ M}\Omega \leq R \leq 22 \text{ M}\Omega$	48 dE	
Humidity	JIS C 5202 7.5;	I,000 hours; 40±2 °C; 93(+2/–3)% RH	±(0.5%+0.05 Ω) for 1%	ś tol.	
Humidity (steady state)	JIS C 5202 7.5; IEC 60115-8 4.24.8	I,000 hours; 40±2 °C; 93(+2/–3)% RH RCWV applied for I.5 hours on and 0.5 hour off	\pm (0.5%+0.05 Ω) for 1% \pm (2.0%+0.05 Ω) for 5%		
•	•	·	,		
(steady state)	IEC 60115-8 4.24.8	RCWV applied for 1.5 hours on and 0.5 hour off	$\pm (2.0\% + 0.05 \ \Omega)$ for 5%		
(steady state) Leaching Intermittent	IEC 60115-8 4.24.8 EIA/IS 4.13B;	RCWV applied for 1.5 hours on and 0.5 hour off Solder bath at 260 ± 5 °C Dipping time: 30 ± 1 seconds	$\pm (2.0\% + 0.05 \ \Omega)$ for 5%	ś tol.	
(steady state) Leaching	IEC 60115-8 4.24.8 EIA/IS 4.13B; IEC 60115-8 4.18	RCWV applied for 1.5 hours on and 0.5 hour off Solder bath at 260 ± 5 °C Dipping time: 30 ± 1 seconds	$\pm (2.0\% + 0.05 \ \Omega)$ for 5% No visible damage	s tol.	
(steady state) Leaching Intermittent	IEC 60115-8 4.24.8 EIA/IS 4.13B; IEC 60115-8 4.18	RCWV applied for 1.5 hours on and 0.5 hour off Solder bath at 260±5 °C Dipping time: 30±1 seconds At room temperature; 2.5 × RCWV applied for 1 second on and 25 seconds off; total 10,000	$\pm (2.0\% + 0.05~\Omega)$ for 5% No visible damage $\pm (1.0\% + 0.05~\Omega)$ for 1%	s tol.	
Leaching Intermittent Overload Resistance to	IEC 60115-8 4.24.8 EIA/IS 4.13B; IEC 60115-8 4.18 JIS C 5202 5.8	RCWV applied for 1.5 hours on and 0.5 hour off Solder bath at 260±5 °C Dipping time: 30±1 seconds At room temperature; 2.5 × RCWV applied for 1 second on and 25 seconds off; total 10,000 cycles	$\pm (2.0\% + 0.05~\Omega)$ for 5% No visible damage $\pm (1.0\% + 0.05~\Omega)$ for 1%	6 tol.	
Leaching Intermittent Overload Resistance to Vibration	IEC 60115-8 4.24.8 EIA/IS 4.13B; IEC 60115-8 4.18 JIS C 5202 5.8 On request	RCWV applied for 1.5 hours on and 0.5 hour off Solder bath at 260±5 °C Dipping time: 30±1 seconds At room temperature; 2.5 × RCWV applied for 1 second on and 25 seconds off; total 10,000 cycles On request	$\pm (2.0\% + 0.05~\Omega)$ for 5% No visible damage $\pm (1.0\% + 0.05~\Omega)$ for 1% $\pm (2.0\% + 0.05~\Omega)$ for 5%	ś tol. ś tol. tol.	

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REVISION HISTORY

REVISION	DATE	CHANGE NOTIFICATION	DESCRIPTION
Version I	Aug 02, 2004	-	- New datasheet for 0603 thick film 1% and 5% with lead-free terminations
			- Replace the 0603 part of pdf files: RC01_11_21_31_5, RC02_12_22_32_10, and HRC21_5_4
			- Test method and procedure updated
			- PE tape added (paper tape will be replaced by PE tape)
			- High ohmic products combined into standard products.
Version 2	Aug 19, 2004	-	-

