

AN7523

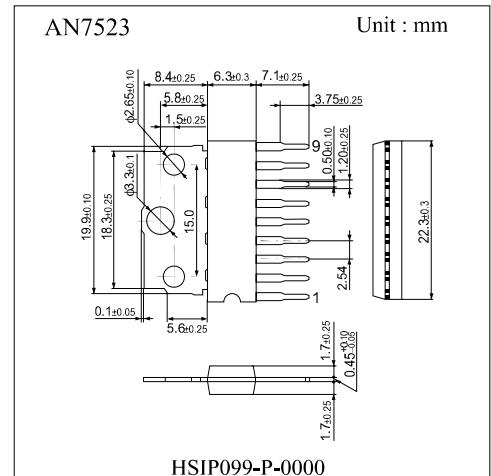
3W BTL Audio Power Amplifier Circuit

■ Features

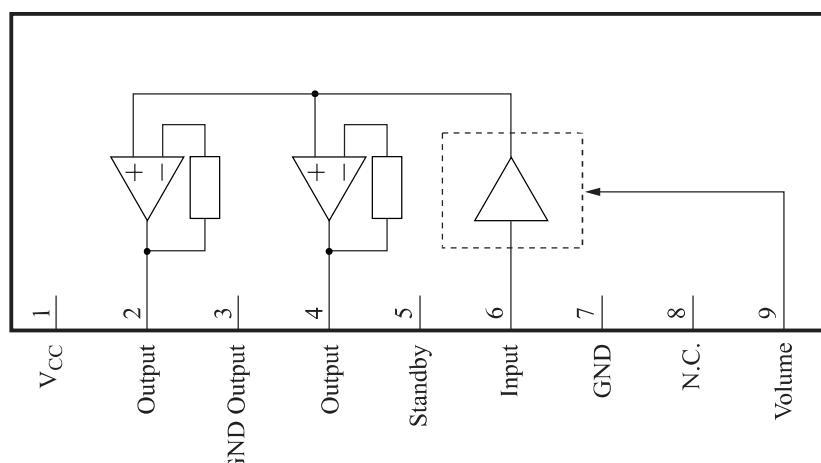
- V_{CC}=8V, Output=3W(8Ω)
- Built-in Standby function.
- Built-in DC volume circuits.

■ Applications

- TVs, Audio equipment



■ Block Diagram



■ Pin Descriptions

Pin No.	Function
1	Vcc
2	ch1 Output(+)
3	GND(Output1)
4	ch1 Output(-)
5	Standby
6	ch1 Input
7	GND
8	N.C
9	DC volume

■ Absolute Maximum Ratings

Parameter	Symbol	Ratio	Unit	Note
Storage temperature	T _{stg}	-55 to +150	°C	1
Operating ambient temperature	T _{opr}	-25 to +70	°C	1
Supply voltage	V _{cc}	14	V	2
Supply current	I _{cc}	1.0	A	
Power dissipation	PD	1220	mW	T _a =70°C

Note1) Ta=25°C except storage temperature and operating ambient temperature.

Note2) At no-signal.

■ Operating Supply Voltage Range

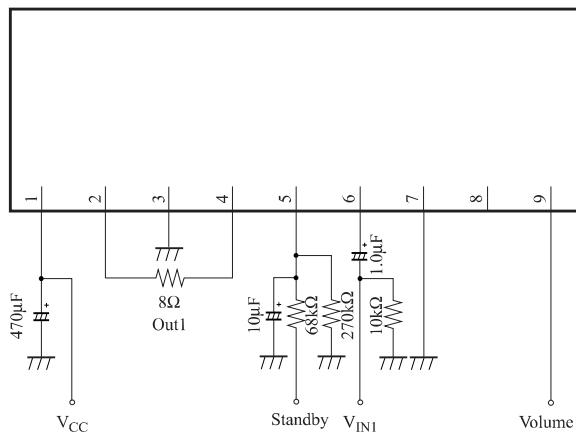
Operating supply voltage range	V _{cc}	3.5V to 13.5V
--------------------------------	-----------------	---------------

■ Electrical Characteristics ($V_{CC}=5.0V$, $R_L=8\Omega$, freq=1kHz, $T_a=25^{\circ}C \pm 2^{\circ}C$)

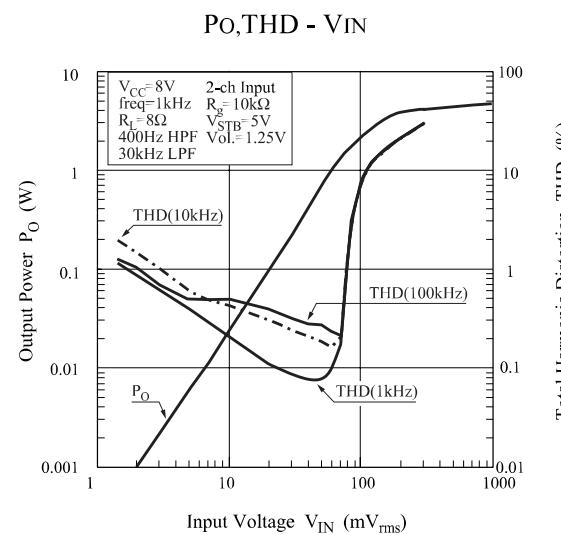
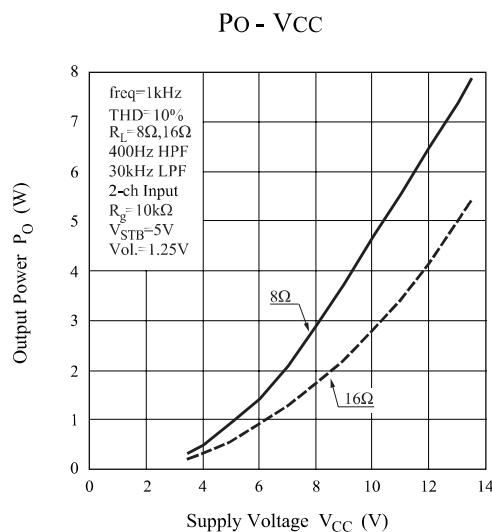
Parameter	Symbol	Condition	min.	typ.	max.	Unit	Note
Quiescent current	I_{CQ}	$V_{IN}=0mV$, Vol.=0V	—	25	60	mA	
Standby current	I_{STB}	$V_{IN}=0mV$, Vol.=0V	—	1	10	μA	
Output noise voltage	V_{NO}	$R_g=10k\Omega$, Vol.=0V	—	0.10	0.4	mVRms	1
Voltage gain	G_V	$P_o=0.25W$, Vol.=1.25V	31	33	35	dB	
Total harmonic distortion	THD	$P_o=0.25W$, Vol.=1.25V	—	0.10	0.5	%	
Maximum power output	P_o	$THD=10\%$, Vol.=1.25V	2.4	3.0	—	W	
Ripple rejection ratio	RR	$R_g=10k\Omega$, Vol.=0V $V_r=0.5V$ rms, fr=120Hz	30	50	—	dB	1
Output offset voltage	V_{off}	$R_g=10k\Omega$, Vol.=0V	-250	0	250	mV	
Maximum attenuation	Att	$P_o=0.5W$, Vol=0V	70	85	—	dB	1
Center voltage gain	G_{VM}	$P_o=0.5W$, Vol=0.6V	20.5	23.5	26.5	dB	
Standby terminal current	I_{STB}	$V_{IN}=0mV$, $V_{STB}=3V$	—	—	25	μA	
Volume terminal current	I_{vol}	$V_{IN}=0mV$, Vol=0V	-12	—	—	μA	

Note1) For this measurement,use the filter <Bandwidth:15Hz to 30kHz(12dB/octave)>

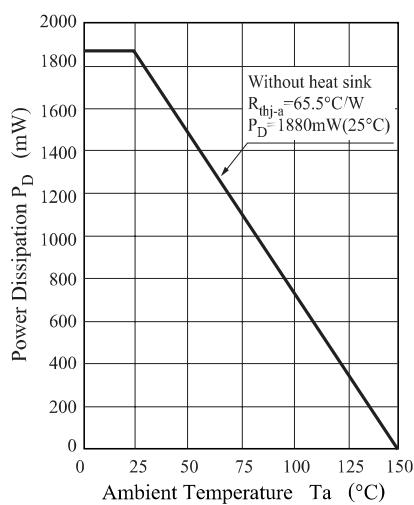
■ Application Circuit



■ Characteristic Curve



■ Package Power Dissipation



■ Printed Board Circuit Layout

