18020 Hobart Blvd., Unit B Gardena, CA 90248 U.S.A

Tel.: (310) 767-1052 Fax: (310) 767-7958

Data Sheet No. BRDB-1000-1C

ABDB-1000-1C

## 10 AMP SILICON BRIDGE RECTIFIERS

#### **FEATURES**

- **VOID FREE VACUUM DIE SOLDERING FOR MAXIMUM** MECHANICAL STRENGTH AND HEAT DISSIPATION (Solder Voids: Typical < 2%, Max. < 10% of Die Area)
- **BUILT-IN STRESS RELIEF MECHANISM FOR** SUPERIOR RELIABILITY AND PERFORMANCE
- SURGE OVERLOAD RATING TO 400 AMPS PEAK
- **RECOGNIZED FILE #E124962**
- Rohs Compliant

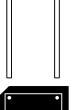
## **MECHANICAL DATA**

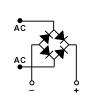
- Case: Molded Epoxy (UL Flammability Rating 94V-0)
- Terminals: Round silver plated copper pins
- Soldering: Per MIL-STD 202 Method 208 guaranteed
- Polarity: Marked on side of case; positive lead at beveled corner
- Mounting Position: Any. Through hole provided for #6 screw
- Weight: 0.18 Ounces (5.4 Grams)

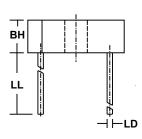
#### MECHANICAL SPECIFICATION

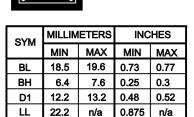


## SERIES DB1000-DB1010 and ADB1004-ADB1008





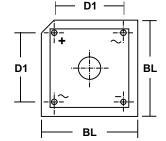




1.3

0.048

0.052



## **MAXIMUM RATINGS & ELECTRICAL CHARACTERISTICS**

LD

1.2

Ratings at 25 °C ambient temperature unless otherwise specified. Single phase, half wave, 60Hz, resistive or inductive load. For capacitive loads, derate current by 20%.

| PARAMETER (TEST CONDITIONS)   | SYMBOL            |   |             |             |            |            |            |            |            |            |                          |       |
|---|-------------------|---|-------------|-------------|------------|------------|------------|------------|------------|------------|--------------------------|-------|
|   |                   | CONTROLLED NON-CONTROLLED AVALANCHE AVALANCHE |             |             |            |            |            |            |            |            |                          | UNITS |
| Series Number   |                   | ADB<br>1004                                   | ADB<br>1006 | ADB<br>1008 | DB<br>1000 | DB<br>1001 | DB<br>1002 | DB<br>1004 | DB<br>1006 | DB<br>1008 | DB<br>1010               |       |
| Maximum DC Blocking Voltage   | <b>V</b> RM       |   |             |             |            |            |            |            |            |            |                          |       |
| Working Peak Reverse Voltage  | <b>V</b> RWM      | 400   | 600         | 800         | 50         | 100        | 200        | 400        | 600        | 800        | 1000                     | VOLTS |
| Maximum Peak Recurrent Reverse Voltage  | <b>V</b> RRM      |   |             |             |            |            |            |            |            |            |                          |       |
| RMS Reverse Voltage   | VR (RMS)          | 280   | 420         | 560         | 35         | 70         | 140        | 280        | 420        | 560        | 700                      |       |
| Power Dissipation in V(BR) Region for 100 $\mu\text{S}$ Square Wave   | PRM               | 500   |             |             | n/a        |            |            |            |            |            |                          |       |
| Continuous Power Dissipation in V(BR) Region @ THS=80°C (Heat Sink Temp)                                      | Pr                | 2   |             |             | n/a        |            |            |            |            |            |                          | WATTS |
| Thermal Energy (Rating for Fusing)  | l²t               | 64  |             |             |            |            |            |            |            |            | AMPS <sup>2</sup><br>SEC |       |
| Peak Forward Surge Current. Single 60Hz Half-Sine Wave Superimposed on Rated Load (JEDEC Method). TJ = 150° C | IFSM              | 400   |             |             |            |            |            |            |            |            | AMPS                     |       |
| Average Forward Rectified Current @ Tc = 50°C (Notes 1, 3)<br>@ Ta = 50°C (Note 2)                            | lo                | 10<br>8                                       |             |             |            |            |            |            |            |            |                          |       |
| Junction Operating and Storage Temperature Range  | TJ, TSTG          | -55 to +150                                   |             |             |            |            |            |            |            | °C         |                          |       |
| Minimum Avalanche Voltage   | <b>V</b> (BR) Min | See Note 4                                    |             |             | n/a        |            |            |            |            |            |                          | VOLTS |
| Maximum Avalanche Voltage   | V(BR) Max         | See Note 4                                    |             | n/a         |            |            |            |            |            |            |                          |       |
| Maximum Forward Voltage (Per Diode) at 5 Amps DC  | V <sub>FM</sub>   | 0.95 (Typ. 0.90)                              |             |             |            |            |            |            |            |            |                          |       |
| Maximum Reverse Current at Rated V <sub>RM</sub>  | IRM               | 1<br>50                                       |             |             |            |            |            |            | μ <b>Α</b> |            |                          |       |
| Minimum Insulation Breakdown Voltage (Circuit to Case)  | <b>V</b> iso      | 2000  |             |             |            |            |            |            | VOLTS      |            |                          |       |
| Typical Thermal Resistance Junction to Ambient (Note 2) Junction to Case (Note 1)                             | Rеја<br>Rejc      | 12<br>5                                       |             |             |            |            |            |            |            |            | °C/W                     |       |

NOTES: (1) Bridge mounted on 5.1" x 4.3" x 0.11" thick (12.9cm x 10.8cm x 0.3cm) aluminum plate

(2) Bridge mounted on PC Board with 0.5" sq. (12mm sq.) copper pads and bridge lead length of 0.375" (9.5mm)

(3) Bolt bridge on heat sink with #6 screw, using silicon thermal compound between bridge and mounting surface for maximum heat transfer.

(4) These bridges exhibit the avalanche characteristic at breakdown. If your application requires a specific breakdown voltage range, please contact us.

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Data Sheet No. BRDB-1000-2C ABDB-1000-2C

# 10 AMP SILICON BRIDGE RECTIFIERS

## RATING & CHARACTERISTIC CURVES FOR SERIES DB1000 - DB1010 and SERIES ADB1004 - ADB1008

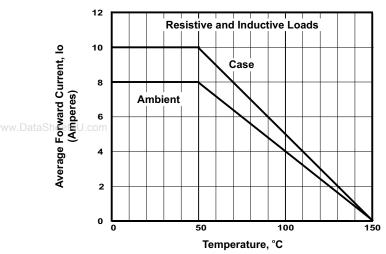
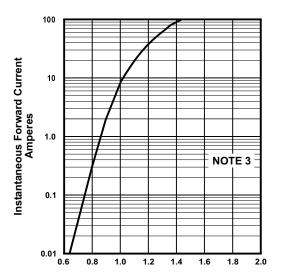
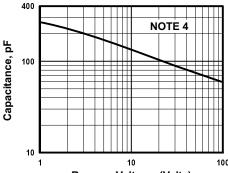


FIGURE 1. FORWARD CURRENT DERATING CURVE



Instantaneous Forward Voltage (Volts)

FIGURE 3. TYPICAL FORWARD CHARACTERISTIC PER DIODE



Reverse Voltage, (Volts)
FIGURE 5. TYPICAL JUNCTION CAPACITANCE PER DIODE

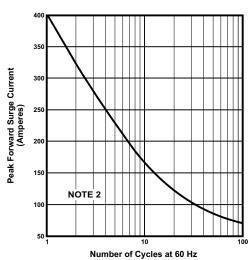
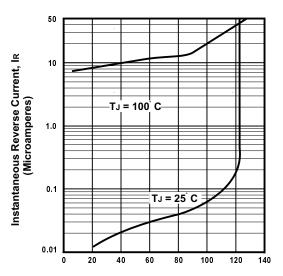


FIGURE 2. MAXIMUM NON-REPETITIVE SURGE CURRENT



Percent of Rated Peak Reverse Voltage
FIGURE 4. TYPICAL REVERSE CHARACTERISTICS

## **NOTES**

(1) Case Temperature, Tc, With Bridge Mounted on 5.1" x 4.3" x 0.11" Thick (12.9cm x 10.8cm x 0.3cm) Aluminum Plate

Ambient Temperature, TA, With Bridge Mounted on PC Board With 0.5" Sq. (12mm Sq.) Copper Pads And Bridge Lead Length of 0.375" (9.5mm)

(2)  $T_J = 150^{\circ}C$ 

(3) T<sub>J</sub> = 25°C; Pulse Width = 300 Sec; 1% Duty Cycle

(4)  $T_J = 25^{\circ}C$ ; f = 1 MHz; Vsig = 50mVp-p