

DRD1320G30

Rectifier Diode

Replaces DS6233-1 DS6233-2 February 2023 (LN42396)

FEATURES

- Double Side Cooling
- High Surge Capability

KEY PARAMETERS

VRRM 3000V IF(AV) 1850A IFSM 20kA

VOLTAGE RATINGS

Part and Ordering Number	Repetitive Peak Voltages VRRM (V)	Conditions
DRD1320G30	3000	
DRD1320G28	2800	VRSM = VRRM + 100V
DRD1320G26	2600	

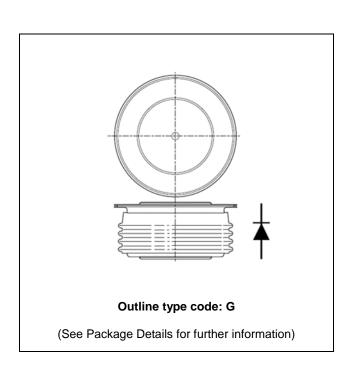


Fig. 1 Package outline

ORDERING INFORMATION

When ordering, select the required part number shown in the Voltage Ratings selection table.

For example:

DRD1320G28 for an 2800V device

Note: Please use the complete part number when ordering and quote this number in any future correspondence relating to your order.

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CURRENT RATINGS

T_{case} = 75°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units			
Double Si	Double Side Cooled						
lf(AV)	Mean forward current	Half wave resistive load	1850	А			
IF(RMS)	RMS value	-	2900	А			
lF	Continuous (direct) forward current	-	2550	А			
Single Sid	Single Side Cooled						
IF(AV)	Mean forward current	Half wave resistive load	1170	А			
IF(RMS)	RMS value	-	1840	А			
lF	Continuous (direct) forward current	-	1500	А			

T_{case} = 100°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units	
Double Side Cooled					
lf(AV)	Mean forward current	Half wave resistive load	1320	Α	
IF(RMS)	RMS value	-	2070	Α	
İF	Continuous (direct) forward current	-	1880	Α	
Single Side Cooled					
I F(AV)	Mean forward current	Half wave resistive load	840	Α	
IF(RMS)	RMS value	-	1320	Α	
lF	Continuous (direct) forward current	-	1130	Α	

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SURGE RATINGS

Symbol	Parameter	Test Conditions	Max.	Units
IFSM	Surge (non-repetitive) forward current	10ms half sine, T _{case} = 175°C	16	kA
l²t	I ² t for fusing	Vr = 50% Vrrm - 1/4 sine	1.28	MA ² s
IFSM	Surge (non-repetitive) forward current	10ms half sine, T _{case} = 175°C V _R = 0	20	kA
l²t	I ² t for fusing		2	MA ² s

THERMAL AND MECHANICAL RATINGS

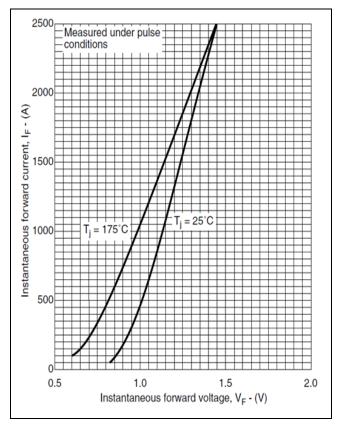
Symbol	Parameter	Test Conditions		Min.	Max.	Units
Rth(j-c)	Thermal resistance - junction to case	Double side cooled	DC	-	32.0	°C/kW
		Single side cooled	Anode DC	-	64.0	°C/kW
			Cathode DC	-	64.0	°C/kW
Rth(c-h)	Thermal resistance - case to heatsink	Clamping force 12.5kN (with mounting compound)	Double side	-	8.0	°C/kW
			Single side	-	16.0	°C/kW
Tvj	Virtual junction temperature	On-state (conducting)		-	185	°C
		Reverse (blocking)		-	175	°C
Tstg	Storage temperature range			-55	200	°C
Fm	Clamping force			11.5	13.5	kN

CHARACTERISTICS

Symbol	Parameter	Test Conditions	Min.	Max.	Units
VFM	Forward voltage	At 1800A peak, Tcase = 25°C	•	1.3	V
lгм	Peak reverse current	At VRRM, Tcase = 175°C	•	50	mA
Qs	Total stored charge	IF = 1000A, dIRR/dt = 3A/µs, Tcase = 175°C, VR = 100V		1600	μC
IRR	Peak reverse recovery current		•	85	А
V T0	Threshold voltage	Tvj = 175°C	-	0.67	V
ľτ	Slope resistance	Tvj = 175°C	-	0.31	mΩ

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CURVES



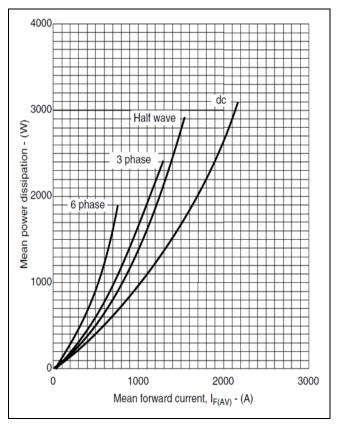


Fig. 2 Maximum & minimum on-state characteristics

Fig. 3 Dissipation curves

VFM EQUATION

 $V_{FM} = A + B.ln(I_F) + C.I_F + D.\sqrt{I_F}$

Where A = 0.82527

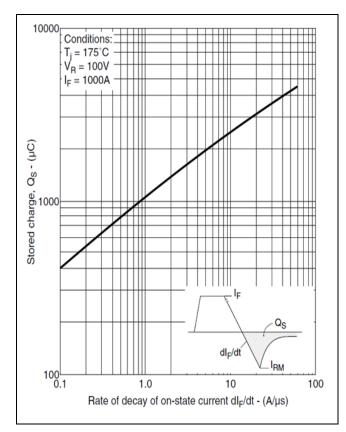
B = -0.07771

C = 0.00012

D = 0.01960

These values are valid for $T_j = 175$ °C for $I_F 500$ A to 2500A

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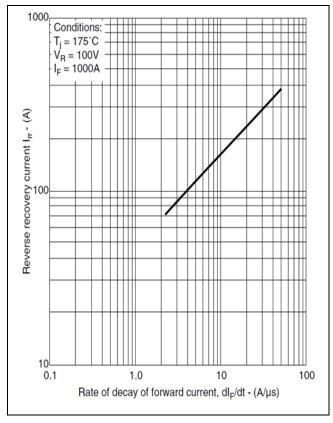


Fig. 4 Total stored charge

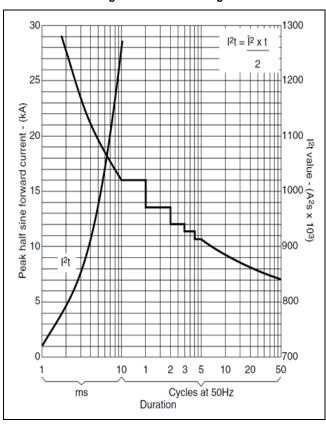


Fig. 6 Surge (non-repetitive) forward current vs time

Fig. 5 Maximum reverse recovery current

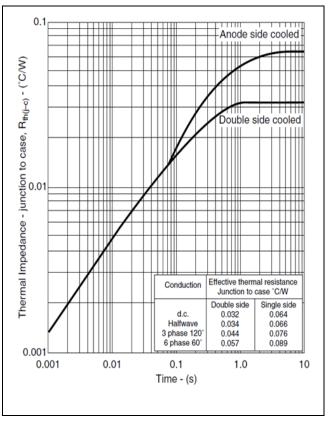


Fig. 7 Maximum (limit) transient thermal impedance - junction to case

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PACKAGE DETAILS

For further package information, please contact Customer services.

All dimensions in mm, unless stated otherwise.

DO NOT SCALE

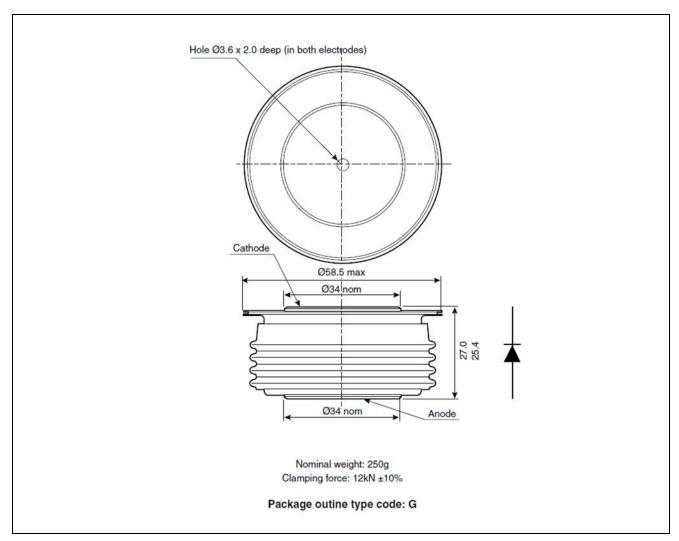


Fig. 8 Package outline

Note:

Some packages may be supplied with gate and or tags.

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