

Product data sheet

1. General description

Planar passivated three quadrant guaranteed commutation triac in a SOT223 surface mountable plastic package for use in motor control circuits or with other highly inductive loads. This triac balances the requirements of commutation performance and gate sensitivity and is intended for use with low power drivers, including microcontrollers.

2. Features and benefits

- 3Q technology for improved noise immunity
- Direct interfacing with low power drivers and microcontrollers
- · Good immunity to false turn-on by dV/dt
- · High commutation capability with sensitive gate
- High voltage capability
- Planar technology for voltage ruggedness and reliability
- Sensitive gate for easy logic level triggering
- Triggering in three quadrants only
- Surface mountable package

3. Applications

- General purpose motor controls
 - Small appliances (White Goods)
- · Loads such as contactors, circuit breakers, valves, dispensers and door locks
- Lower-power highly inductive, resistive and safety loads

4. Quick reference data

Table 1. Q	uick reference data					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V_{DRM}	repetitive peak off-state voltage		-	-	800	V
I _{T(RMS)}	RMS on-state current	full sine wave; T _{sp} ≤ 118 °C; <u>Fig. 1; Fig. 2; Fig. 3</u>	-	-	2	A
I _{TSM}	non-repetitive peak on- state current	full sine wave; T _{j(init)} = 25 °C; t _p = 20 ms; <u>Fig. 4; Fig. 5</u>	-	-	25	A
		full sine wave; $T_{j(init)}$ = 25 °C; t_p = 16.7 ms	-	-	27.5	А
Tj	junction temperature		-	-	150	°C
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static ch	aracteristics					
I _{GT}	gate trigger current	$V_{D} = 12 \text{ V}; I_{T} = 0.1 \text{ A}; \text{ T2+ G+};$ $T_{j} = 25 \text{ °C}; \text{ Fig. 8}$	-	-	10	mA
		$V_{D} = 12 \text{ V}; I_{T} = 0.1 \text{ A}; \text{ T2+ G-};$ T _j = 25 °C; <u>Fig. 8</u>	-	-	10	mA

		$V_{D} = 12 \text{ V}; I_{T} = 0.1 \text{ A}; \text{ T2- G-};$ $T_{j} = 25 \text{ °C}; \text{ Fig. 8}$	-	-	10	mA
I _H	holding current	V _D = 12 V; T _j = 25 °C; <u>Fig. 10</u>	-	-	20	mA
V _T	on-state voltage	I _T = 3 A; T _j = 25 °C; <u>Fig. 11</u>	-	1.15	1.4	V
Dynamic	characteristics					
dV _D /dt	rate of rise of off-state voltage	V_{DM} = 536 V; T _j = 125 °C; (67% of V_{DRM}); exponential waveform; gate open circuit	500	-	-	V/µs
dl _{com} /dt	rate of change of commutating current	$V_D = 400 \text{ V}; T_j = 150 \text{ °C}; I_{T(RMS)} = 2 \text{ A};$ $dV_{com}/dt = 20 \text{ V}/\mu s; (snubberless condition); gate open circuit$	3	-	-	A/ms

5. Pinning information

Table 2. Pinning information								
Pin	Symbol	Description	Simplified outline	Graphic symbol				
1	T1	main terminal 1		NI				
2	T2	main terminal 2		T2-T1				
3	G	gate		Sym051				
4	mb	mounting base; connected to main terminal 2						

6. Ordering information

Table 3. Ordering information									
Type number	Package	Orderable part number	Packing	Small packing	Package	Package			
	Name		method	quantity	version	issue date			
BTA202W-800ET	SOT223	BTA202W-800ETF	Reel	4000	SOT223	16-Mar-2006			

7. Marking

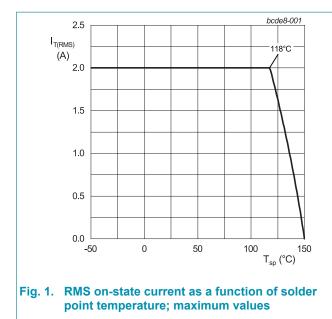
Table 4. Marking codes							
Type number	Marking codes						
BTA202W-800ET	B2W8ET						

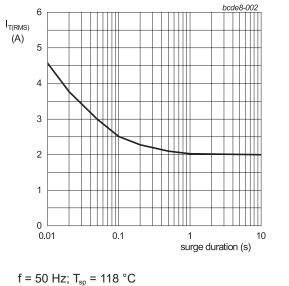
8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

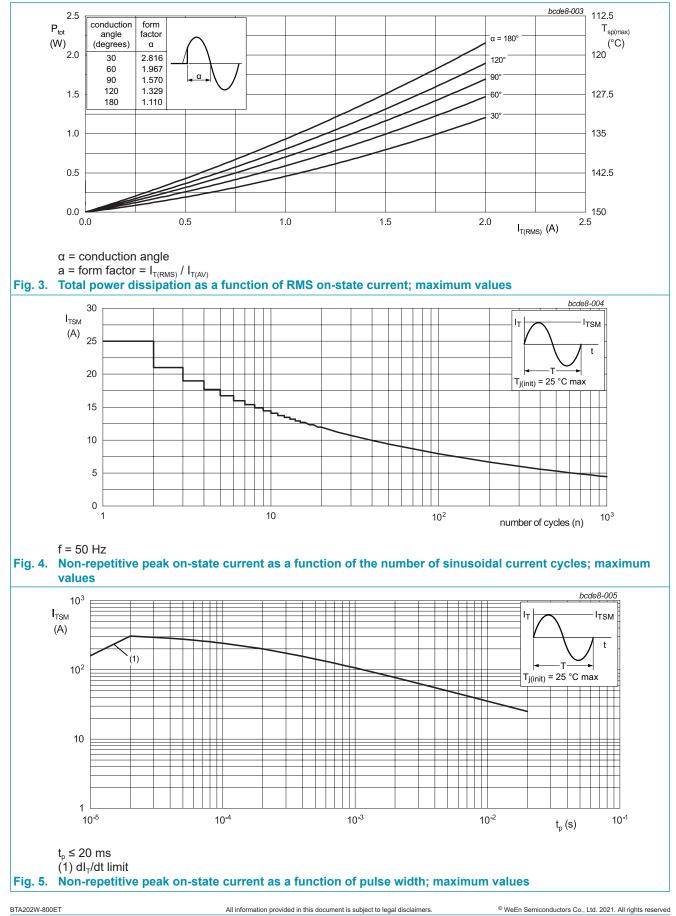
Symbol	Parameter	Conditions	Min	Max	Unit
V_{DRM}	repetitive peak off-state voltage		-	800	V
$I_{T(RMS)}$	RMS on-state current	full sine wave; T _{sp} ≤ 118 °C; <u>Fig. 1; Fig. 2; Fig. 3</u>	-	2	A
I _{TSM}	non-repetitive peak on- state current	full sine wave; $T_{j(init)}$ = 25 °C; t_p = 20 ms; Fig 4; Fig 5	-	25	A
		full sine wave; $T_{j(init)}$ = 25 °C; t_p = 16.7 ms	-	27.5	А
l ² t	I ² t for fusing	t _p = 10 ms; sine-wave pulse	-	3	A ² s
dl _T /dt	rate of rise of on-state current	I _G = 20 mA	-	100	A/µs
I _{GM}	peak gate current		-	2	А
P_{GM}	peak gate power		-	5	W
$P_{G(AV)}$	average gate power	over any 20 ms period	-	0.5	W
T _{stg}	storage temperature		-40	150	°C
Tj	junction temperature		-	150	°C





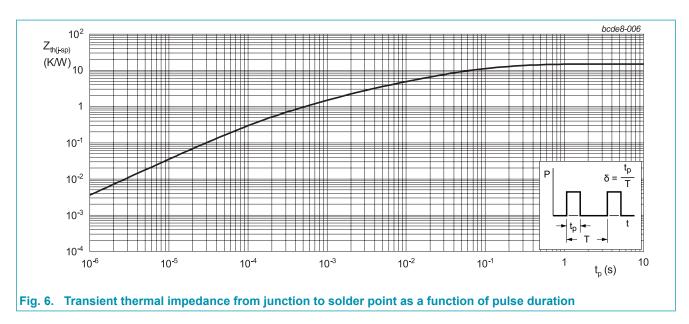


BTA202W-800ET 3Q Hi-Com Triac



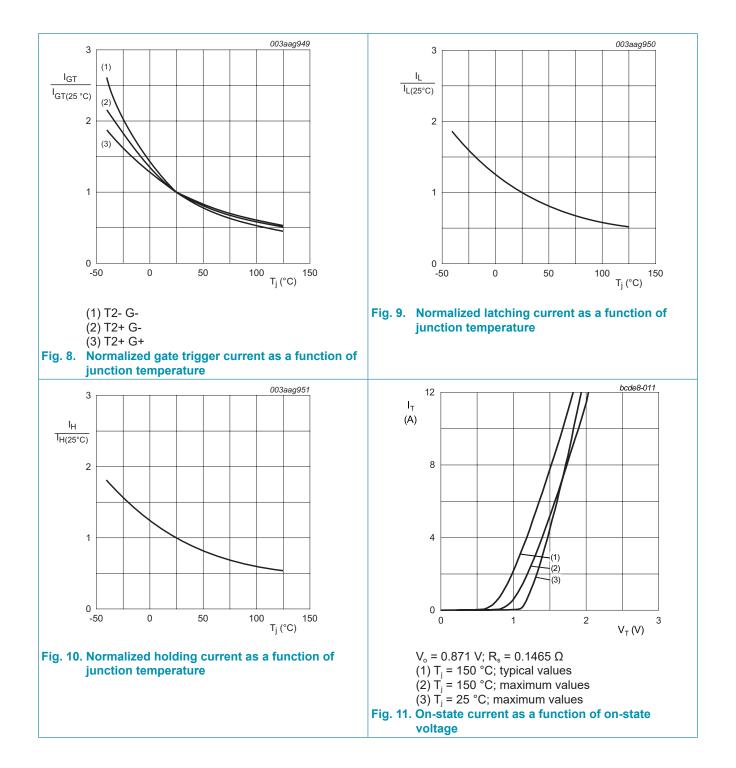
9. Thermal characteristics

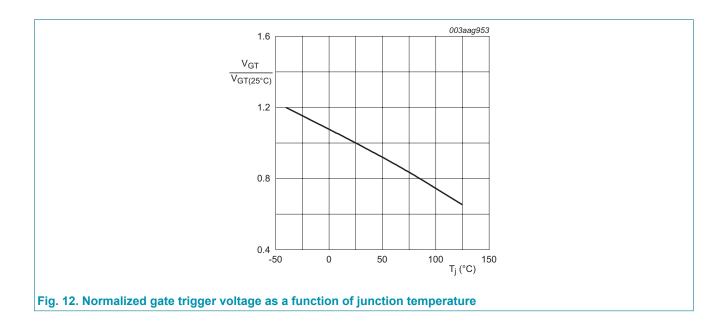
Table 6. Th	ermal characteristics		 			
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$R_{th(j-sp)}$	thermal resistance from junction to solder point	full cycle or half cycle; <u>Fig 6</u>	-	-	15	K/W
$R_{th(j-a)}$	thermal resistance from junction to ambient free air	in free air	-	156	-	K/W



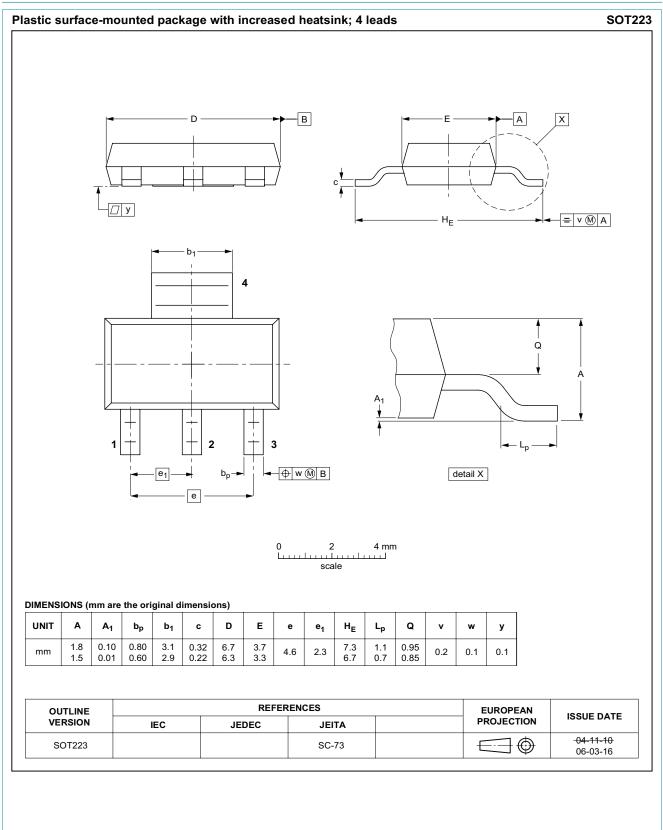
10. Characteristics

Symbol	Parameter	Conditions	IV	lin	Тур	Max	Unit
Static cha	aracteristics						
Ι _{GT}	gate trigger current	$V_{D} = 12 \text{ V}; \text{ I}_{T} = 0.1 \text{ A}; \text{ T2+ G+};$ T _j = 25 °C; Fig. 8	-		-	10	mA
		V_{D} = 12 V; I _T = 0.1 A; T2+ G-; T _j = 25 °C; Fig. 8	-		-	10	mA
		V _D = 12 V; I _T = 0.1 A; T2- G-; T _j = 25 °C; <u>Fig. 8</u>	-		-	10	mA
IL	latching current	V_{D} = 12 V; I _G = 0.1 A; T2+ G+; T _j = 25 °C; Fig. 9	-		-	30	mA
		V _D = 12 V; I _G = 0.1 A; T2+ G-; T _j = 25 °C; <u>Fig. 9</u>	-		-	40	mA
		V _D = 12 V; I _G = 0.1 A; T2- G-; T _j = 25 °C; <u>Fig. 9</u>	-		-	30	mA
I _H	holding current	V _D = 12 V; T _j = 25 °C; <u>Fig. 10</u>	-		-	20	mA
V _T	on-state voltage	I _T = 3 A; T _j = 25 °C; <u>Fig. 11</u>	-		1.15	1.4	V
V_{GT}	gate trigger voltage	$V_{D} = 12 \text{ V}; \text{ I}_{T} = 0.1 \text{ A}; \text{ T}_{J} = 25 \text{ °C};$ Fig. 12	-		0.8	1	V
		$V_{\rm D}$ = 400 V; I _T = 0.1 A; T _j = 125 °C	0	.2	0.45	-	V
I _D	off-state current	V _D = 800 V; T _j = 25 °C	-		-	5	μA
		V _D = 800 V; T _j = 125 °C	-		-	0.5	mA
Dynamic	characteristics		· · · · · ·				
dV _D /dt	rate of rise of off-state voltage	V_{DM} = 536 V; T _j = 125 °C; (67% of V_{DRM}); exponential waveform; gate open circuit	5	00	-	-	V/µs
		V_{DM} = 536 V; T_j = 150 °C; (67% of V_{DRM}); exponential waveform; gate open circuit	2	00	-	-	V/µs
dl _{com} /dt	rate of change of commutating current	$V_D = 400 \text{ V}; \text{ T}_j = 150 \text{ °C}; \text{ I}_{T(RMS)} = 2 \text{ A};$ $dV_{com}/dt = 20 \text{ V}/\mu\text{s}; \text{ gate open circuit};$ (snubberless condition)	3		-	-	A/ms





11. Package outline



12. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
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