



#### 150V N-CHANNEL ENHANCEMENT MODE MOSFET

#### **Product Summary**

BV <sub>DSS</sub>	Rds(ON) Max	I <sub>D</sub> Max T <sub>C</sub> = +25°C	
150V	60mΩ @ V <sub>GS</sub> = 10V	21A	

### **Description and Applications**

This new generation MOSFET features low on-resistance and fast switching, making it ideal for high efficiency power management applications.

- Power management functions
- DC-DC converters
- Backlighting

## **Features and Benefits**

- 100% Unclamped Inductive Switching Ensures More Reliable and Robust End Application
- Low R<sub>DS(ON)</sub> Minimizes Power Losses
- Low Q<sub>g</sub> Minimizes Switching Losses
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative. https://www.diodes.com/quality/product-definitions/

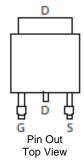
#### Mechanical Data

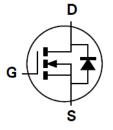
- Package: TO252
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 3 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.33 grams (Approximate)





Top View





**Equivalent Circuit** 

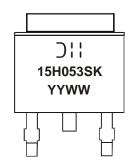
## **Ordering Information** (Note 4)

Part Number	Packago	Packing		
	Package	Qty.	Carrier	
DMT15H053SK3-13	TO252 (DPAK)	2500	Tape & Reel	

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

# **Marking Information**



The Manufacturer's Code Marking 15H053SK = Product Type Marking Code YYWW = Date Code Marking YY = Last Two Digits of Year (ex: 22 = 2022) WW = Week Code (01 to 53)



#### **Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	V <sub>DSS</sub>	150	V	
Gate-Source Voltage	Vgss	±20	V	
Continuous Drain Current V 40V (Note 5)	Tc = +25°C	- I <sub>D</sub>	21	Α
Continuous Drain Current, V <sub>GS</sub> = 10V (Note 5)	Tc = +70°C		17	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I <sub>DM</sub>	84	Α	
Maximum Continuous Body Diode Forward Current (Note 5)	Is	21	Α	
Pulsed Body Diode Continuous Current (10µs Pulse, Duty Cycle	Isм	84	Α	
Avalanche Current, L = 1mH	IAS	11.7	Α	
Avalanche Energy, L = 1mH	Eas	68.4	mJ	

## Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 6)		PD	1.7	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R <sub>0JA</sub>	75	°C/W
Total Power Dissipation (Note 7)		PD	2.8	W
Thermal Resistance, Junction to Ambient (Note 7)	Steady State	Reja	45	°C/W
Total Power Dissipation (Note 5)		PD	60	W
Thermal Resistance, Junction to Case (Note 5)		R <sub>0</sub> JC	2.1	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

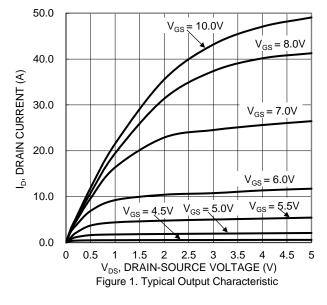
## Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

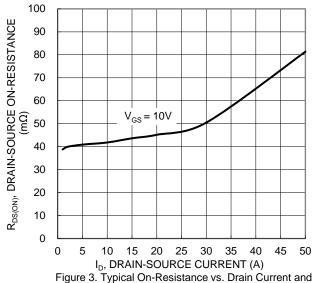
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)			, , , , , , , , , , , , , , , , , , ,				
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	150	_	_	V	V <sub>G</sub> S = 0V, I <sub>D</sub> = 10mA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_	_	1	μΑ	V <sub>DS</sub> = 120V, V <sub>GS</sub> = 0V	
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V <sub>GS(TH)</sub>	2	3	4	V	$V_{DS} = V_{GS}$ , $I_D = 250\mu A$	
Static Drain-Source On-Resistance	RDS(ON)	_	43	60	mΩ	V <sub>G</sub> S = 10V, I <sub>D</sub> = 20A	
Diode Forward Voltage	V <sub>SD</sub>	_	0.9	1	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 20A	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	C <sub>iss</sub>		814	_		V <sub>DS</sub> = 75V, V <sub>GS</sub> = 0V f = 1MHz	
Output Capacitance	Coss	_	84	_	pF		
Reverse Transfer Capacitance	Crss		3.7				
Gate Resistance	Rg	_	0.6	_	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$	
Total Gate Charge	Qg	_	11.5	_		V <sub>DS</sub> = 75V, I <sub>D</sub> = 4.1A,	
Gate-Source Charge	Qgs	_	4.6	_	nC		
Gate-Drain Charge	Qgd	_	2.8			$V_{GS} = 10V$	
Turn-On Delay Time	t <sub>D</sub> (ON)	_	8.5	_			
Turn-On Rise Time	t <sub>R</sub>	_	3.4	_		$V_{DS} = 75V, V_{GS} = 10V,$ $I_{D} = 4.1A, R_{g} = 6\Omega$	
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	11.9	_	ns		
Turn-Off Fall Time	tF	_	6.2	_			
Reverse Recovery Time	t <sub>RR</sub>	_	47	_	ns	1 4 4 4 3 12/34 4 4 0 0 4 / 2 2	
Reverse Recovery Charge	Qrr	_	87	_	nC	F = 4.1A, di/dt = 100A/μs	

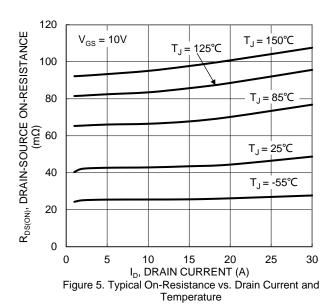
Notes:

- 5. Thermal resistance from junction to soldering point (on the exposed drain pad).6. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
- 7. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
- 8. Short duration pulse test used to minimize self-heating effect.
- 9. Guaranteed by design. Not subject to product testing.

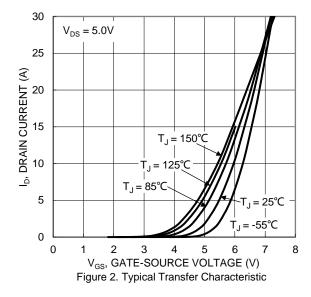


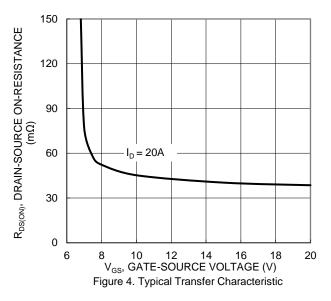






Gate Voltage





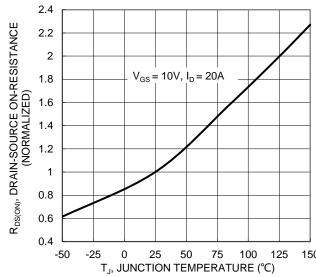
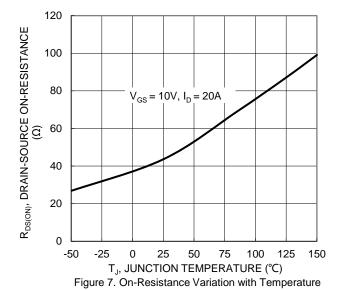


Figure 6. On-Resistance Variation with Temperature







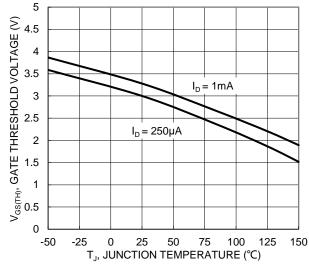
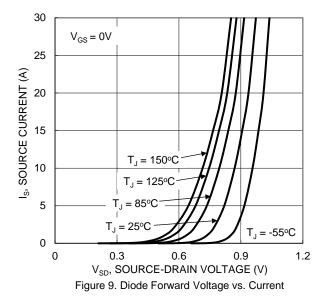
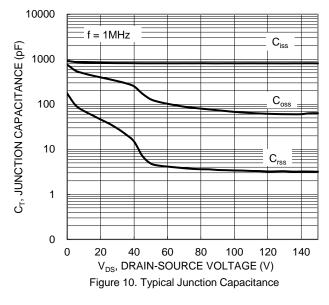
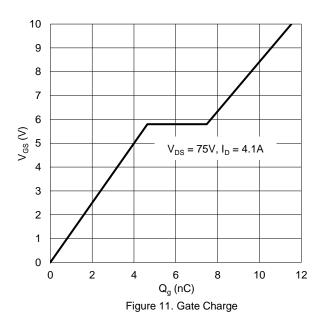
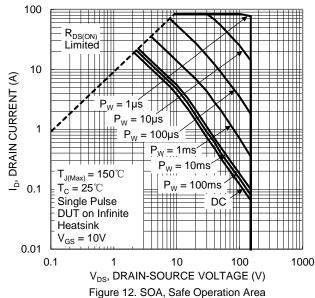


Figure 8. Gate Threshold Variation vs. Junction Temperature











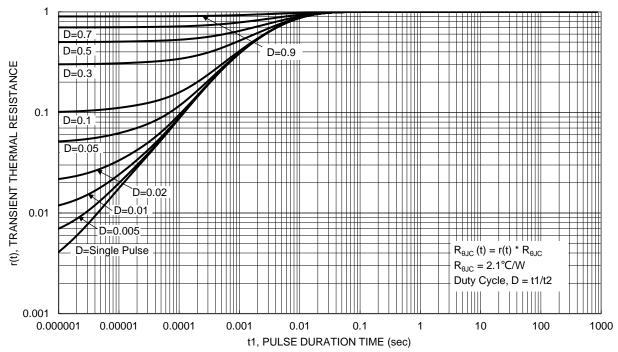


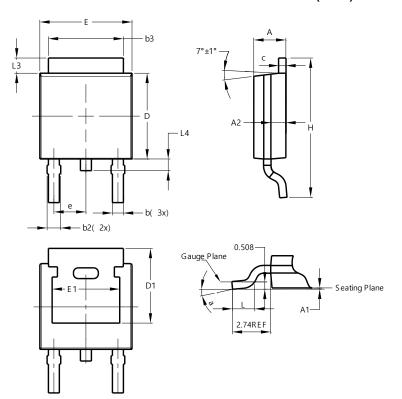
Figure 13. Transient Thermal Resistance



# **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### TO252 (DPAK)

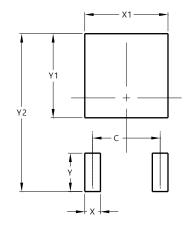


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TO252 (DPAK)					
Dim	Min	Max	Тур		
Α	2.19	2.39	2.29		
<b>A</b> 1	0.00	0.13	0.08		
<b>A2</b>	0.97	1.17	1.07		
b	0.64	0.88	0.783		
b2	0.76	1.14	0.95		
b3	5.21	5.46	5.33		
С	0.45	0.58	0.531		
D	6.00	6.20	6.10		
D1	5.21	-	-		
е	-	-	2.286		
Е	6.45	6.70	6.58		
E1	4.32	-	-		
Н	9.40	10.41	9.91		
L	1.40	1.78	1.59		
L3	0.88	1.27	1.08		
L4	0.64	1.02	0.83		
а	0°	10°	-		
All Dimensions in mm					

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### **TO252 (DPAK)**



Dimensions	Value (in mm)		
С	4.572		
Х	1.060		
X1	5.632		
Υ	2.600		
Y1	5.700		
Y2	10.700		



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