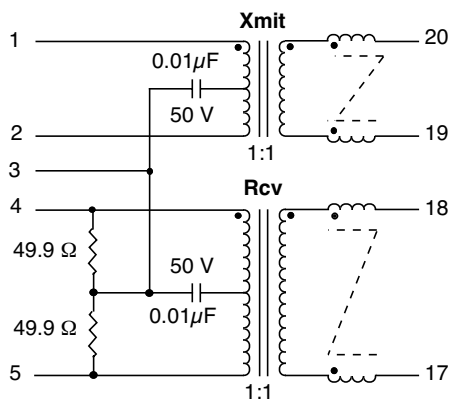


- Designed for IMR2 Chip
- 8 Cores with Discrete Components
- Robust construction allows for toughest reflow processing
- Complies with or exceeds IEEE 802.3, 10Base-T Standards
- 1500 Vrms Isolation (for 1 minute)

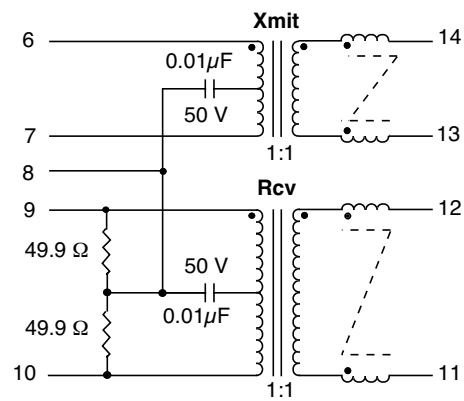
Electrical Parameters @ 25° C

OCL (μ H Min.)	Insertion Loss† (dB Max.)	Return Loss (dB Min.)	Impedance (Ω)	Common Mode Rejection (Ω)	Crosstalk (dB Min.)
100 KHz, 0.1 Vrms	1-10 MHz	5-10 MHz		30-100 MHz	1-10 MHz
Pins 1-2,4-5,6-7,9-10	Xmit/Rcv	Xmit/Rcv			
200	-1	-15	100	-30	-30

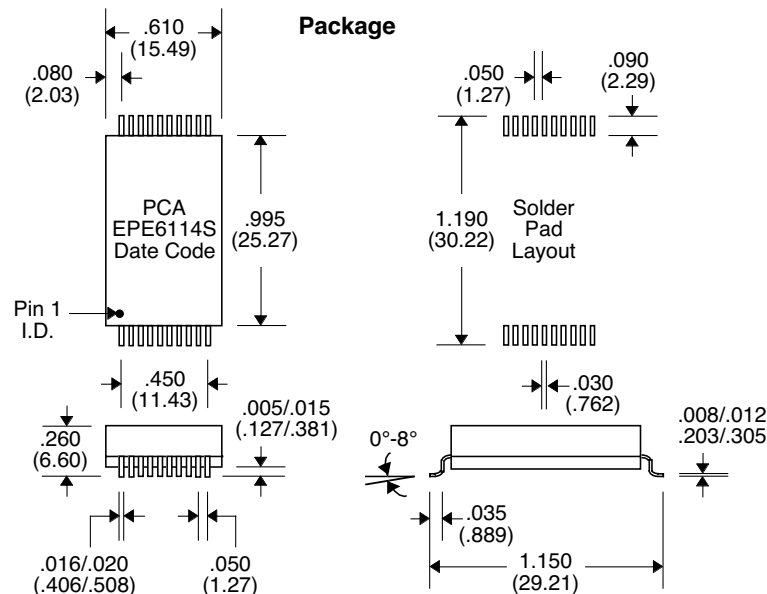
• †Excludes all Resistor Networks for Receive Side •



Schematic



Package



Unless Otherwise Specified Dimensions are in Inches /mm ± .010 / .25